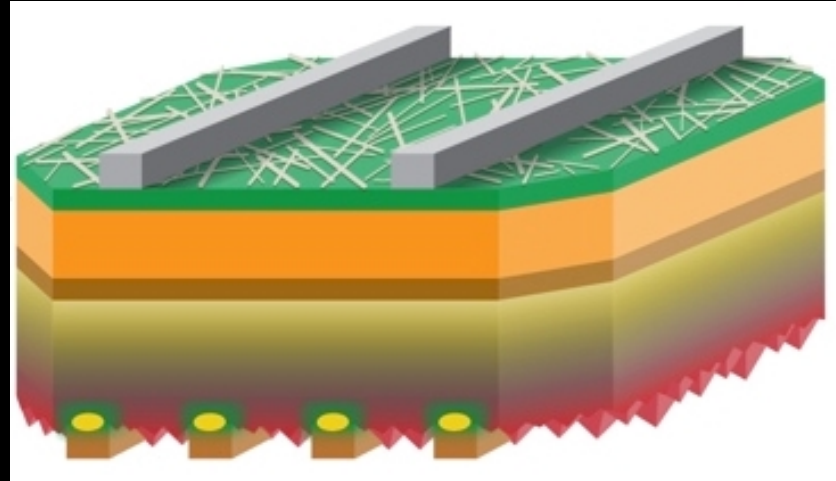


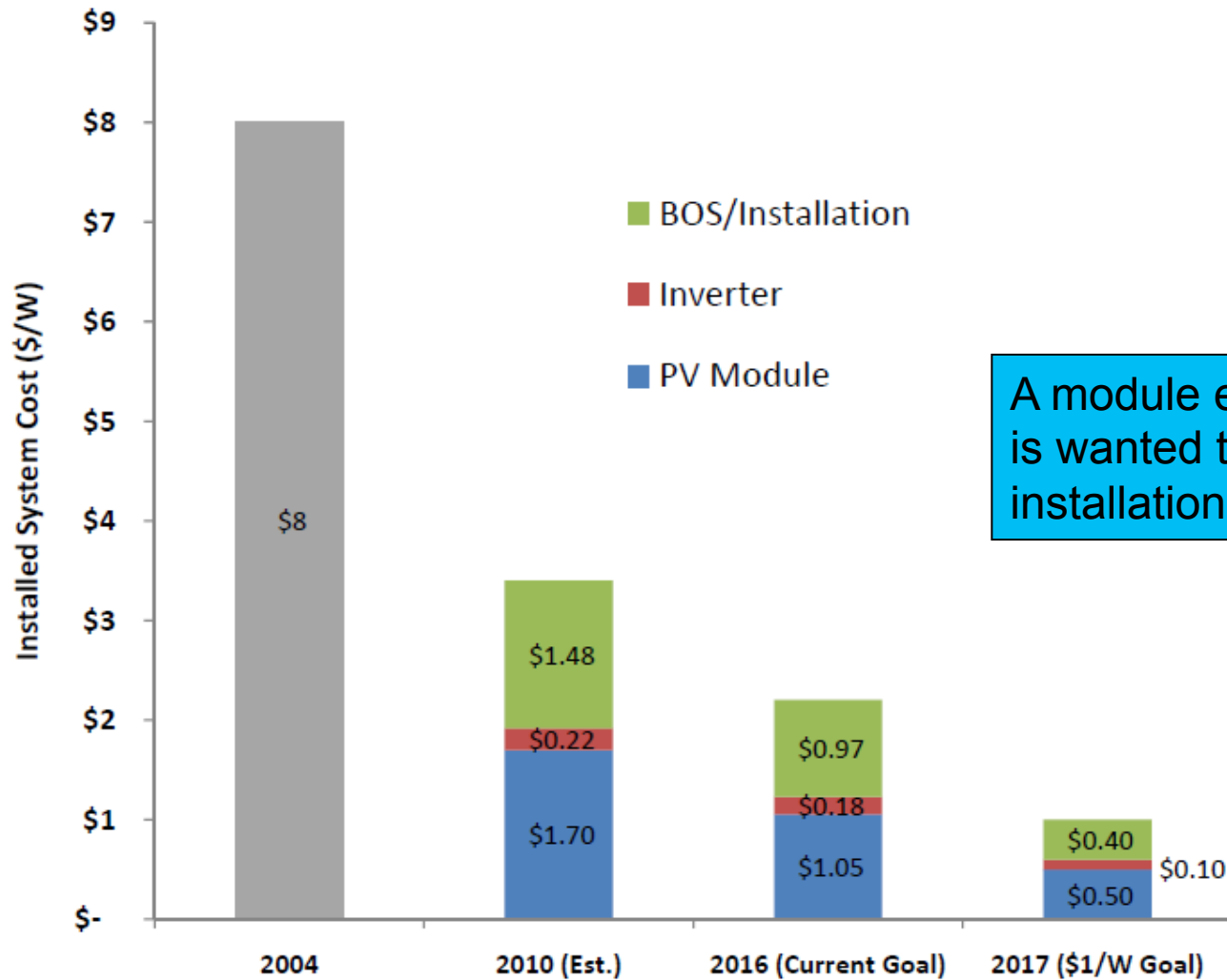
# Emerging High-Efficiency Low-Cost Solar Cell Technologies



Mike McGehee

Materials Science and Engineering  
Center for Advanced Molecular Photovoltaics  
Bay Area Photovoltaic Consortium  
Precourt Institute for Energy  
Stanford University

# DOE's Sunshot Goal: \$1/W by 2017

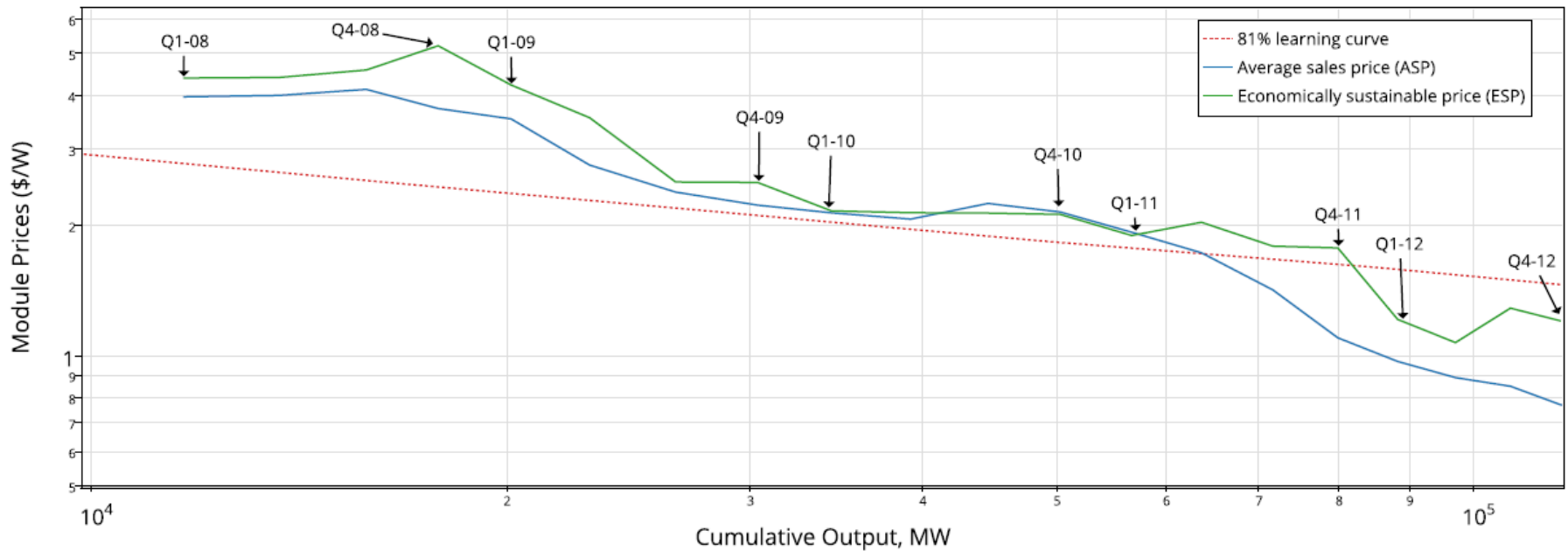


A module efficiency of 25 % is wanted to enable the installation cost reductions.

# Last Week's Lecture by Anshu Sahoo and Stefan Reichelstein

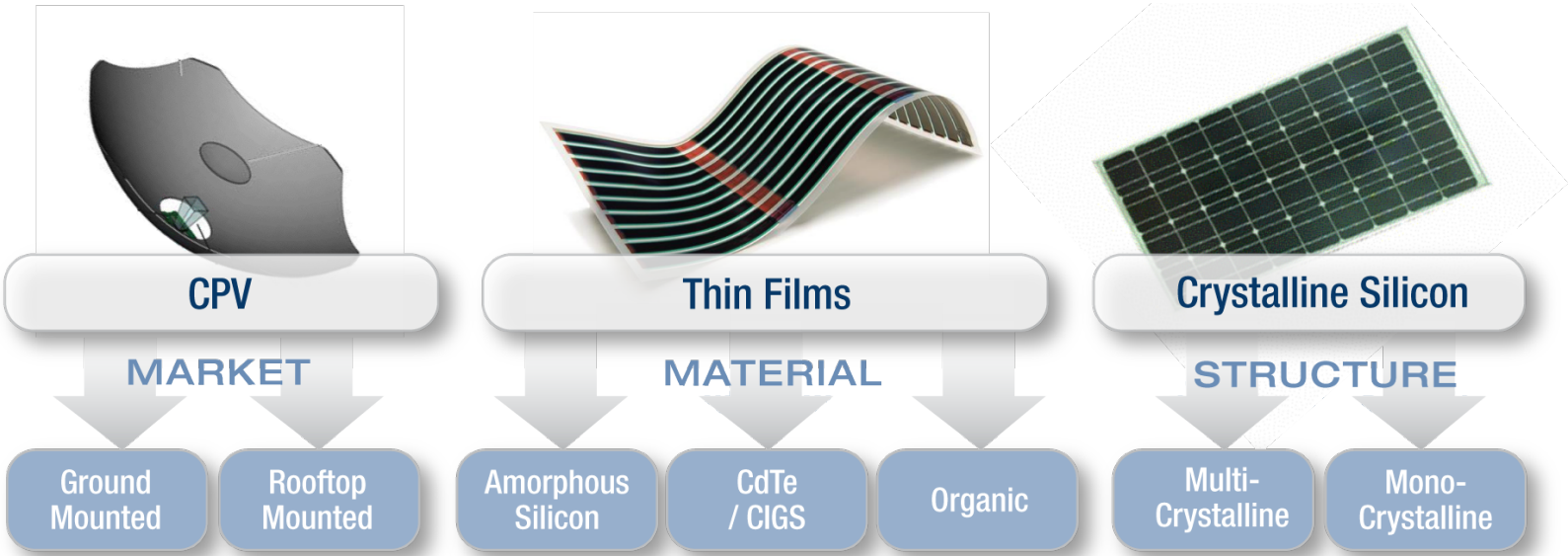
Silicon is currently competitive in favorable locations with the current subsidies.

Average Sales Prices of Modules and Cumulative Module Output



In 2017 the cost of silicon cells will probably be \$0.65/W.

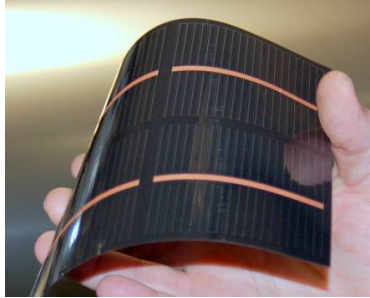
# There are many approaches to making PV cells and experts do not agree on which one is the best



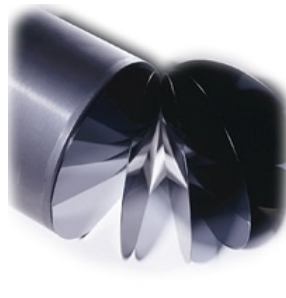
20x-100x



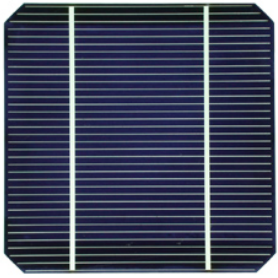
500x



$\text{Cu(In,Ga)Se}_2 \sim 1\text{-}2 \text{ }\mu\text{m}$

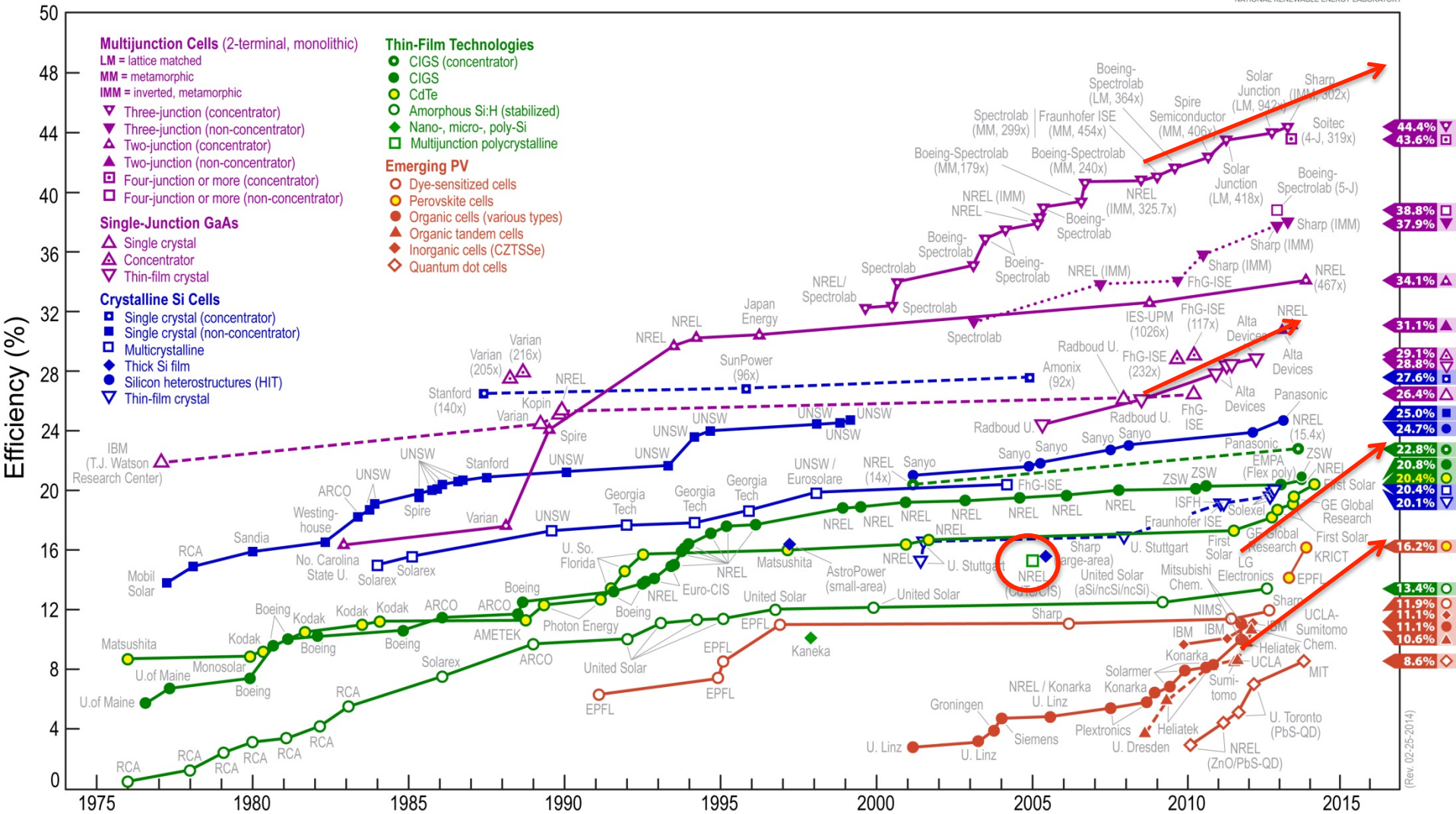


c-Si  $\sim 180 \text{ }\mu\text{m}$





# Best Research-Cell Efficiencies



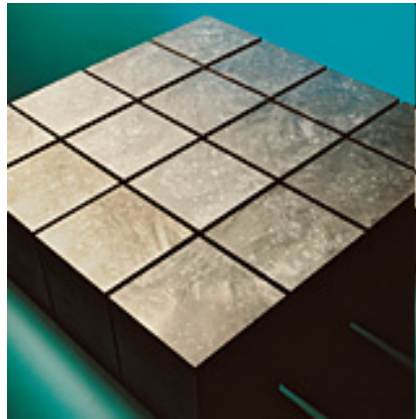
(Rev. 02-25-2014)

Efficiency Records Chart available at: [http://www.nrel.gov/ncpv/images/efficiency\\_chart.jpg](http://www.nrel.gov/ncpv/images/efficiency_chart.jpg). The chart above was downloaded on 2/27/2014.

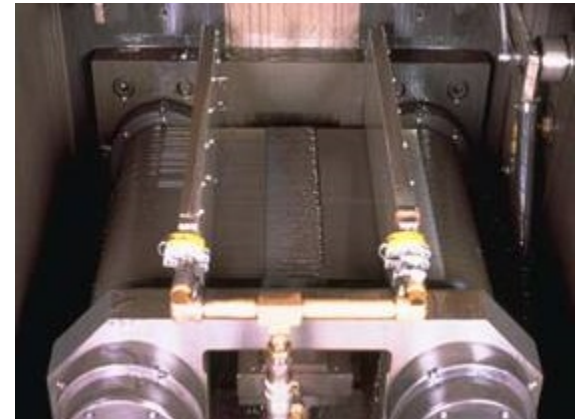
# Silicon PV



Silicon Feedstock



Ingot Growth



Slicing Wafers

Photovoltaic System



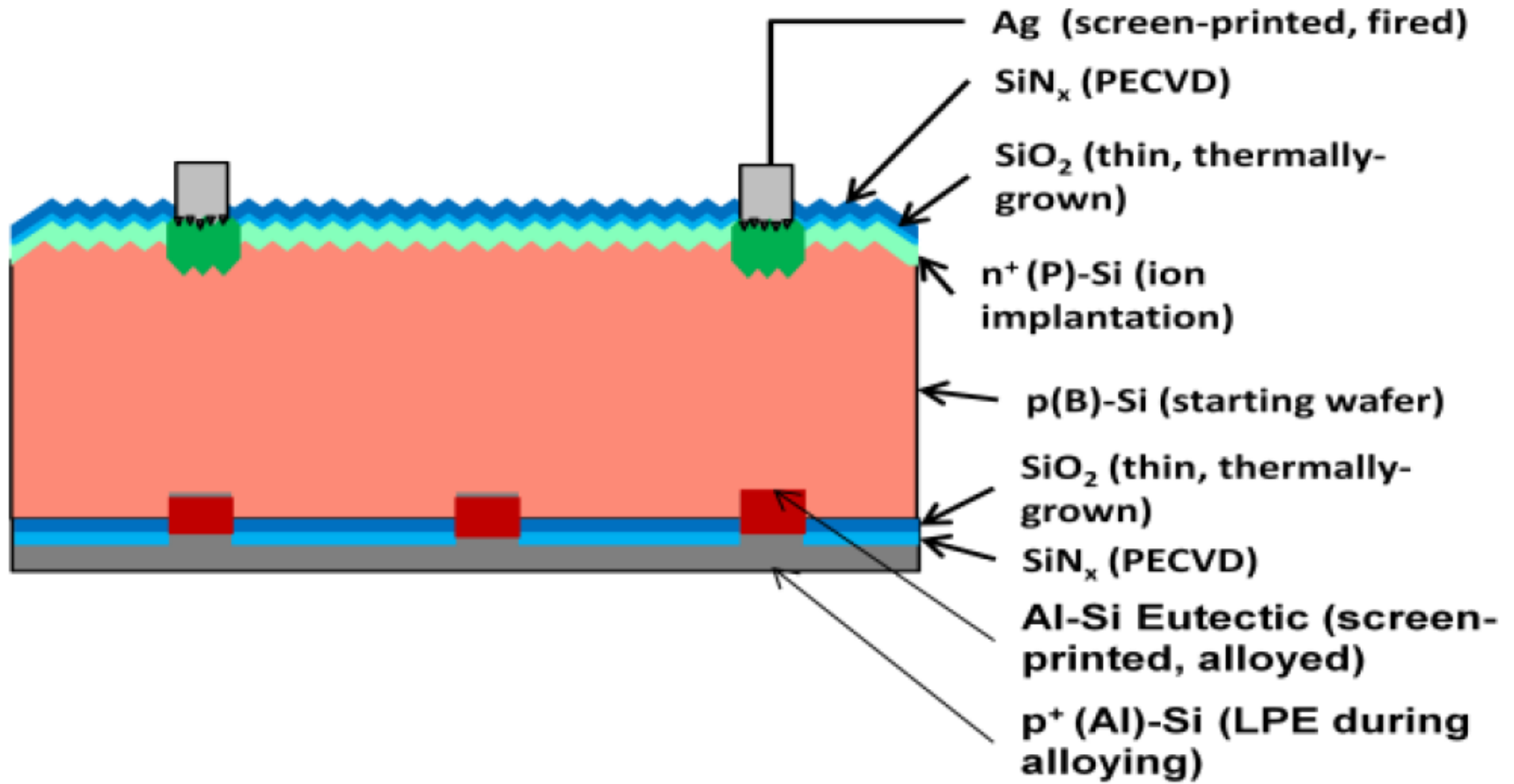
Module Encapsulation



Cell Fabrication

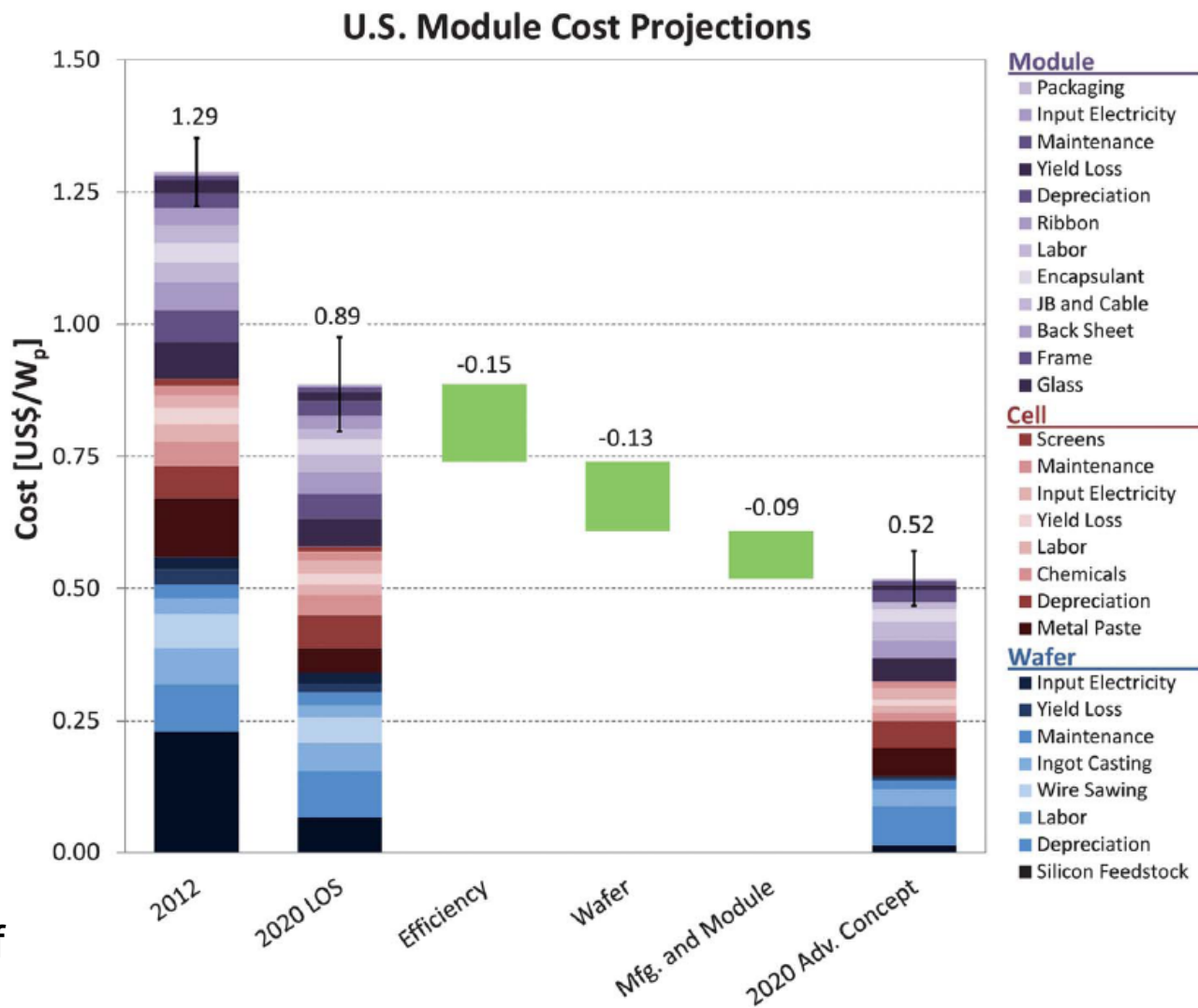


# 19.6% efficient planar cells on silicon



Source: J-H Lai, IEEE PVSC, June 2011

# Cost analysis of Si Modules



LOS is line of sight

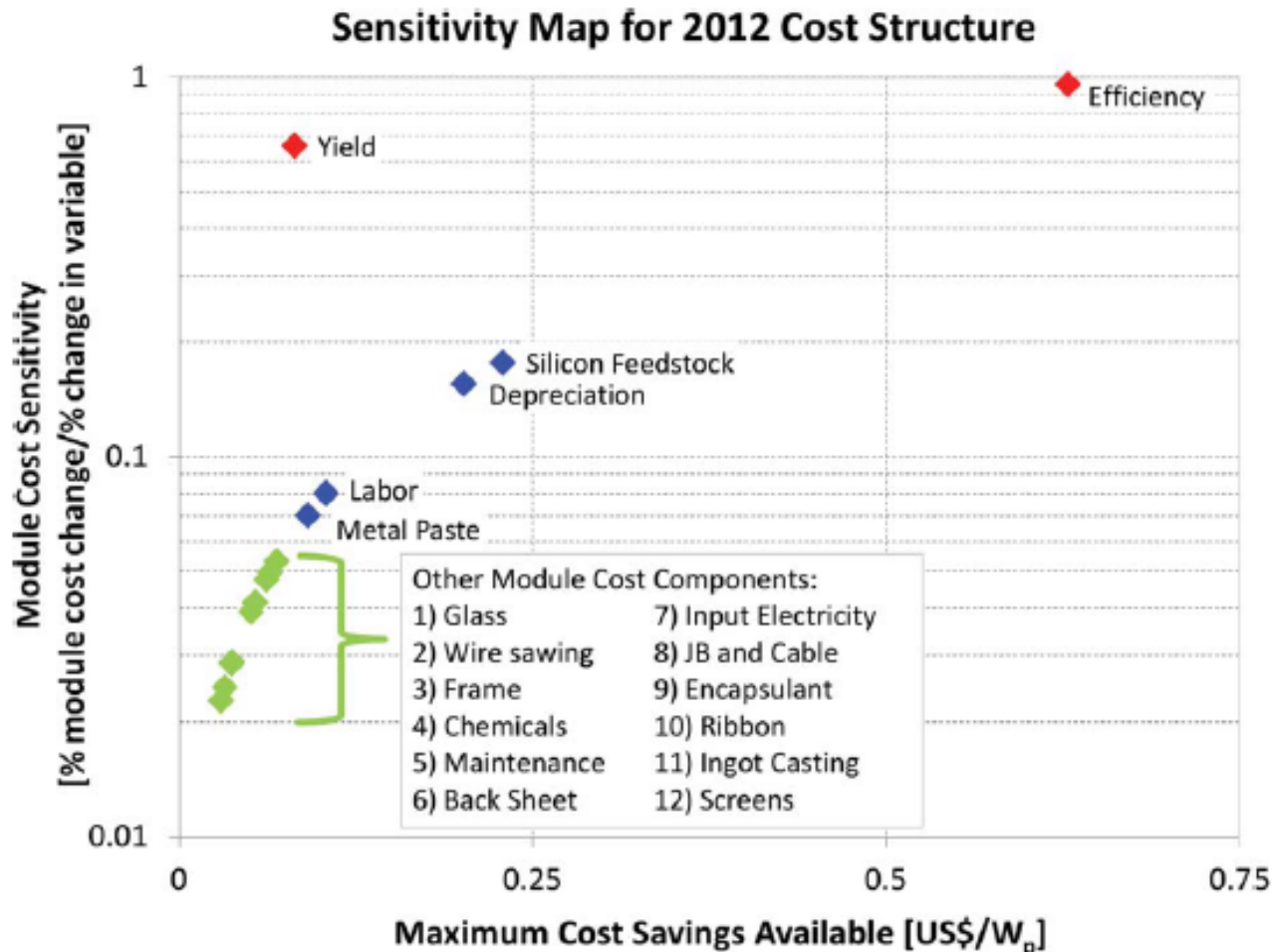


**Table 2** Data table of cost analysis results as displayed in Fig. 2

|                     | 2012<br>Cost [US\$/W <sub>p</sub> ] | 2020 LOS<br>Cost [US\$/W <sub>p</sub> ] | 2020 Adv. Concept<br>Cost [US\$/W <sub>p</sub> ] |
|---------------------|-------------------------------------|---|--|
| <b>★ Wafer</b>      |                                     |   |  |
| ★ Silicon feedstock | 0.229                               | 0.067                                   | 0.014  |
| Depreciation        | 0.090                               | 0.088                                   | 0.073  |
| ★ Labor             | 0.069                               | 0.053                                   | 0.032  |
| ★ Wire sawing       | 0.064                               | 0.049                                   | 0.000  |
| Ingot casting       | 0.029                               | 0.023                                   | 0.000  |
| Maintenance         | 0.027                               | 0.024                                   | 0.018  |
| Yield loss          | 0.028                               | 0.017                                   | 0.004  |
| Input electricity   | 0.024                               | 0.020                                   | 0.003  |
| <b>★ Cell</b>       |                                     |   |  |
| ★ Metal paste       | 0.111                               | 0.047                                   | 0.054  |
| Depreciation        | 0.063                               | 0.061                                   | 0.051  |
| Chemicals           | 0.045                               | 0.039                                   | 0.017  |
| Labor               | 0.034                               | 0.020                                   | 0.013  |
| Yield loss          | 0.030                               | 0.020                                   | 0.011  |
| Input electricity   | 0.024                               | 0.026                                   | 0.021  |
| Maintenance         | 0.018                               | 0.016                                   | 0.013  |
| Screens             | 0.013                               | 0.010                                   | 0.000  |

**Table 2** Data table of cost analysis results as displayed in Fig. 2

|                   | 2012<br>Cost [US\$/W <sub>p</sub> ] | 2020 LOS<br>Cost [US\$/W <sub>p</sub> ] | 2020 Adv. Concept<br>Cost [US\$/W <sub>p</sub> ] |
|-------------------|-------------------------------------|---|--|
| <b>Module</b>     |                                     |   |  |
| Glass             | 0.073                               | 0.056                                   | 0.047  |
| Frame             | 0.060                               | 0.046                                   | 0.000  |
| Back sheet        | 0.050                               | 0.038                                   | 0.032  |
| JB and cable      | 0.040                               | 0.036                                   | 0.036  |
| Encapsulant       | 0.039                               | 0.030                                   | 0.025  |
| Labor             | 0.034                               | 0.020                                   | 0.013  |
| Ribbon            | 0.032                               | 0.025                                   | 0.000  |
| Depreciation      | 0.028                               | 0.028                                   | 0.023  |
| Yield loss        | 0.025                               | 0.017                                   | 0.010  |
| Maintenance       | 0.009                               | 0.008                                   | 0.007  |
| Input electricity | 0.003                               | 0.004                                   | 0.003  |
| Packaging         | 0.003                               | 0.002                                   | 0.002  |
| <b>Total</b>      | <b>1.293</b>                        | <b>0.890</b>                            | <b>0.523</b>                                     |



**Fig. 3** Sensitivity study for the 2012 module cost structure. Input variables that strongly determine module cost are shown toward the top of the plot, while variables that have a large cost reduction potential are shown toward the right.



# World Record 156 mm x 156 mm Full-Square Cell Efficiency Using 43 $\mu\text{m}$ Epitaxial Silicon Cell Absorber



**NREL-Certified Full-Area  
Cell Efficiency = 20.13%**

Device ID: V4-Supreme-16-4106

Device Temperature:  $24.5 \pm 0.5$  °C

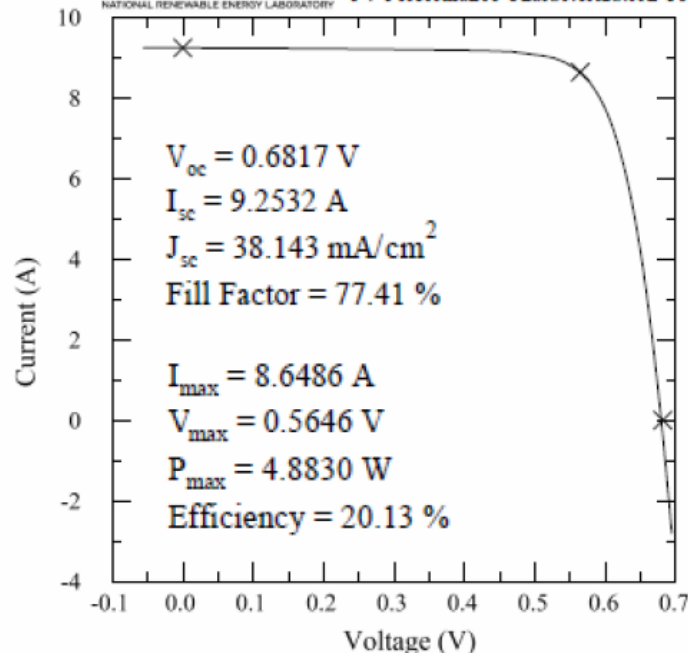
Oct 11, 2012 12:51

Device Area:  $242.6 \text{ cm}^2$

Spectrum: ASTM G173 global

Irradiance:  $1000.0 \text{ W/m}^2$

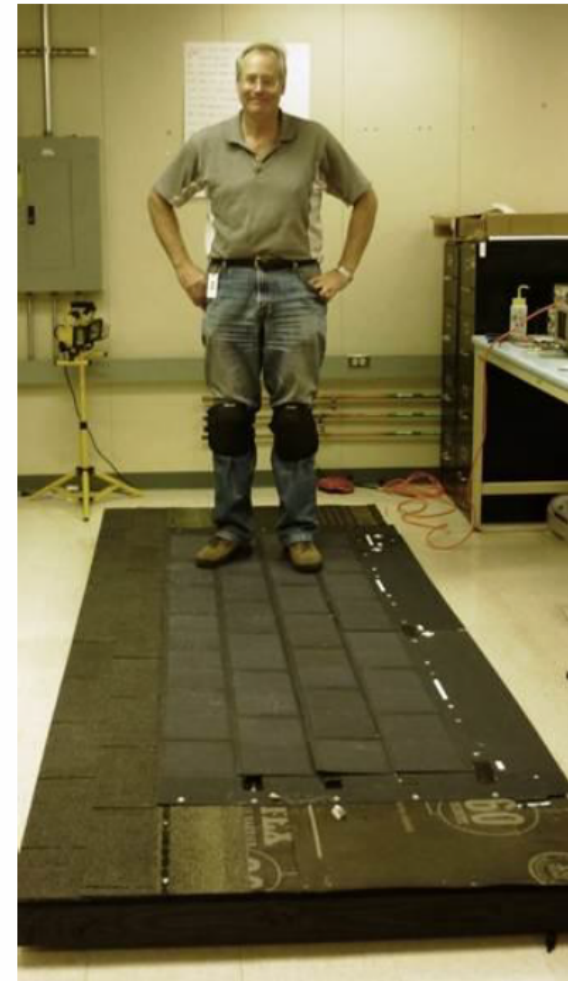
**NREL** X25 IV System  
NATIONAL RENEWABLE ENERGY LABORATORY PV Performance Characterization Team



- \* 156 x 156 mm<sup>2</sup> full-square cell (242.6 cm<sup>2</sup>)
- \* 43  $\mu\text{m}$  epitaxial Si cell, FSRV < 10 cm/s
- \* Voc = 681.7 mV
- \* Jsc = 38.14 mA/cm<sup>2</sup>
- \* FF = 77.41%
- \* Cell Max Power = 4.88 Wp;  $I_{sc}$  = 9.25 A

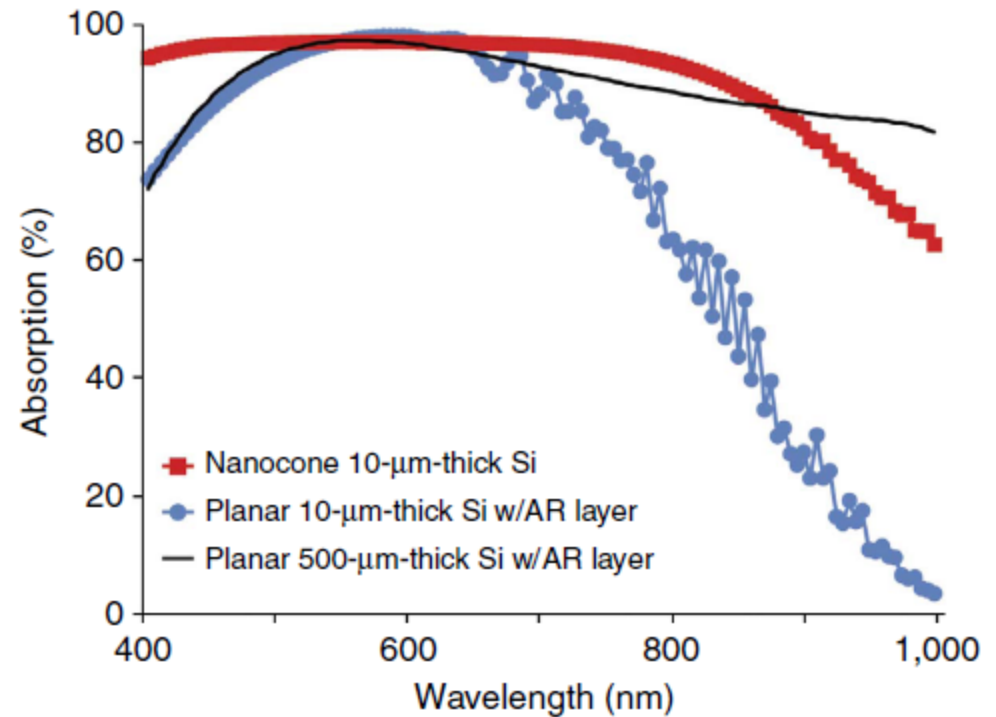
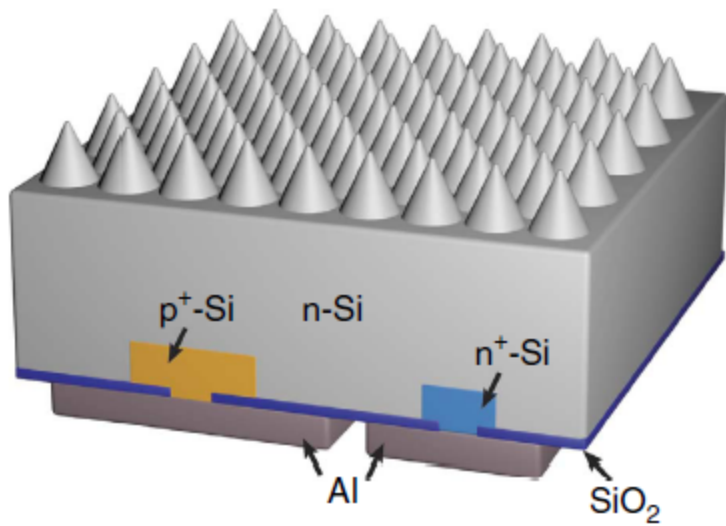


## ✦ Capability of supporting walking



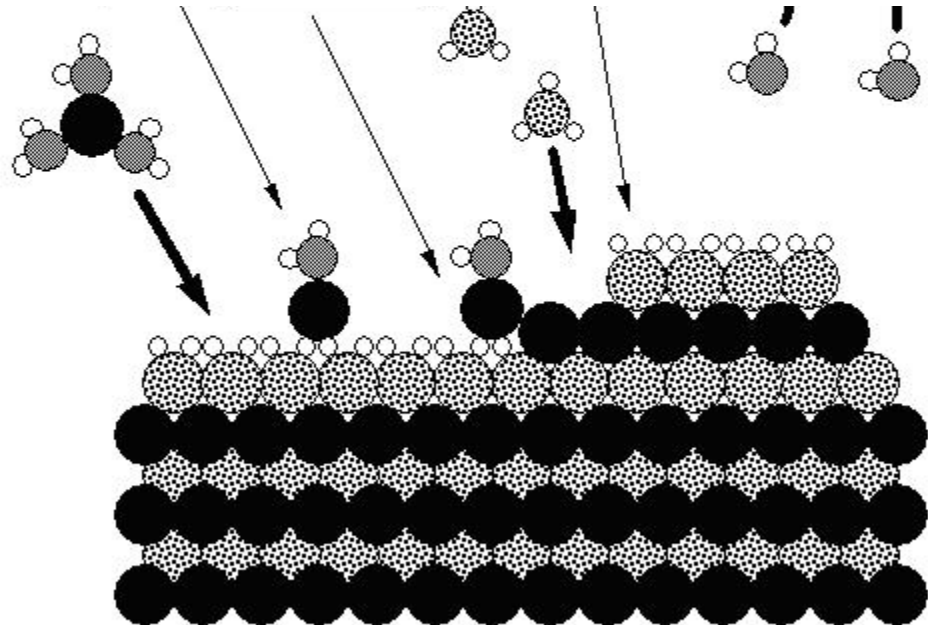
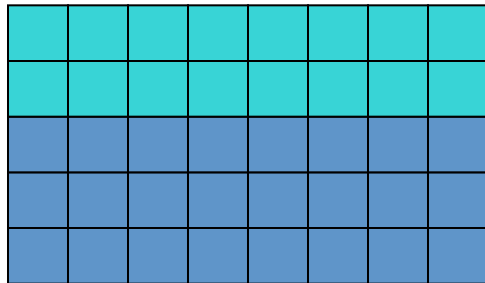
- ✦ Lightweight BIPV solution:
  - ✦ 7 kg/m<sup>2</sup> for BIPV shingle demo system
  - ✦ BIPV is < half the weight of typical framed glass-covered modules
  - ✦ 2/3 the weight of asphalt shingles
  - ✦ Distributed shade management
- ✦ Simple, flat box packaging for shipment and storage

# Using nanocones to enable complete light absorption in thin Si

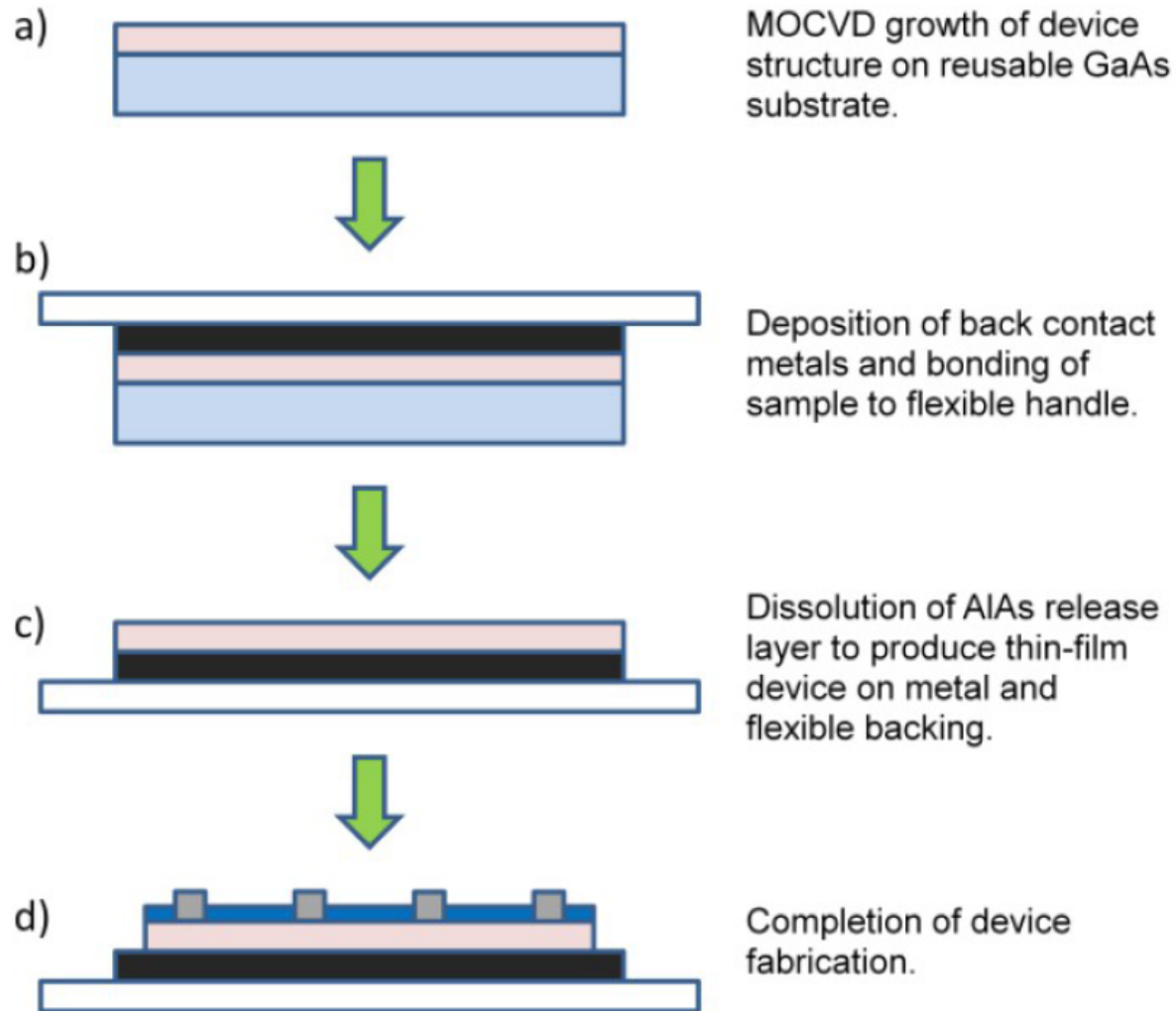


# Gallium Arsenide

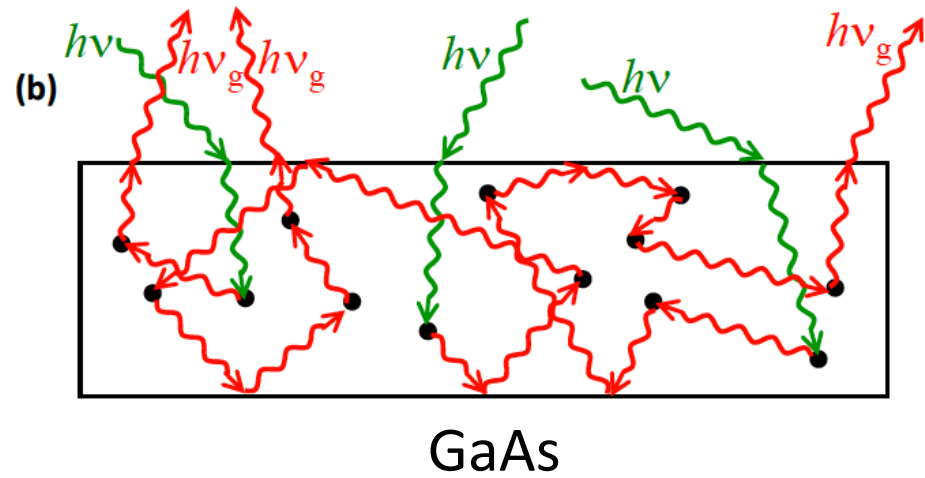
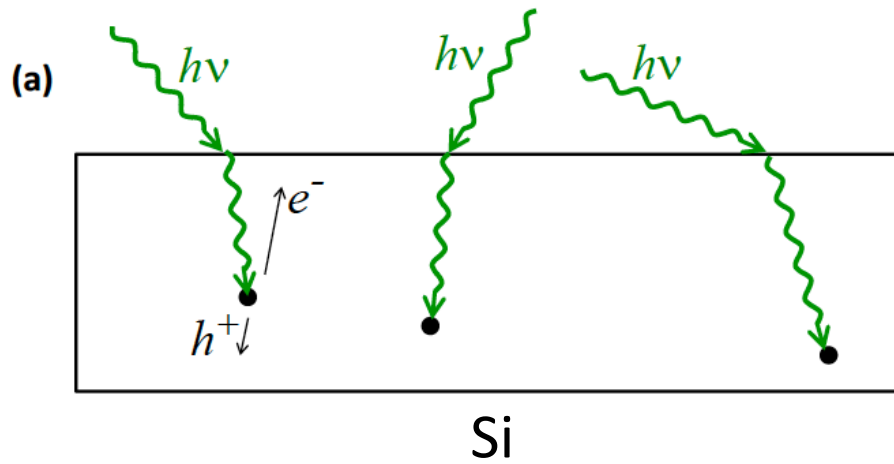
- The 1.4 eV band gap is ideal for solar cells.
- High quality films are grown on single crystal substrates with MOCVD.



# Alta Devices 28.8% efficient thin-film GaAs cell



# Photon recycling



# Why thin film GaAs is better

- Remitted photons are weakly absorbed and can easily travel more than a carrier diffusion length away from the junction in a wafer-based device.
- In a thin cell, a mirror keeps photons near the pn junction.



Textured, good mirror



Untextured, good mirror






Untextured, bad mirror



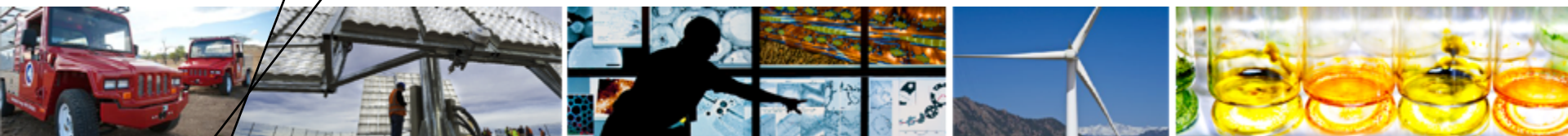
# Theoretical limits

TABLE I  
 $V_{OC}$ ,  $J_{SC}$ , AND EFFICIENCY VALUES FOR THREE POSSIBLE GEOMETRIES AND RELEVANT CELL THICKNESSES

|                           |  |           |            |  |           |            |  |           |            |
|---------------------------|---|-----------|------------|--|-----------|------------|---|-----------|------------|
|                           | Textured, good mirror   |           |            | Untextured, good mirror  |           |            | Untextured, bad mirror  |           |            |
| Thickness                 | 500nm   | 1 $\mu$ m | 10 $\mu$ m | 500nm  | 1 $\mu$ m | 10 $\mu$ m | 500nm   | 1 $\mu$ m | 10 $\mu$ m |
| Voc (volts)               | 1.14  | 1.13      | 1.12       | 1.16   | 1.15      | 1.14       | 1.08  | 1.08      | 1.07       |
| Jsc (mA/cm <sup>2</sup> ) | 32.3  | 32.7      | 33.5       | 29.5   | 31.6      | 32.8       | 25.2  | 29.5      | 32.6       |
| Fill Factor               | 0.89  | 0.89      | 0.89       | 0.89   | 0.89      | 0.89       | 0.89  | 0.89      | 0.89       |
| efficiency %              | 32.8  | 33.1      | 33.4       | 30.6   | 32.6      | 33.3       | 24.3  | 28.3      | 30.9       |

A good rear mirror is crucial to a high open-circuit voltage and, consequently, to efficiencies above 30%.

# A Manufacturing Cost Analysis Relevant to Photovoltaic Cells Fabricated with III-Vs



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**Hydrogen Technologies Center**  
**(Photoelectrolysis Interest)**

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**The National Center for Photovoltaics**

**Aaron Ptak**  
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**Myles Steiner**  
**Pauls Stradins**  
**John Geisz**  
**Daniel Friedman**  
**Sarah Kurtz**

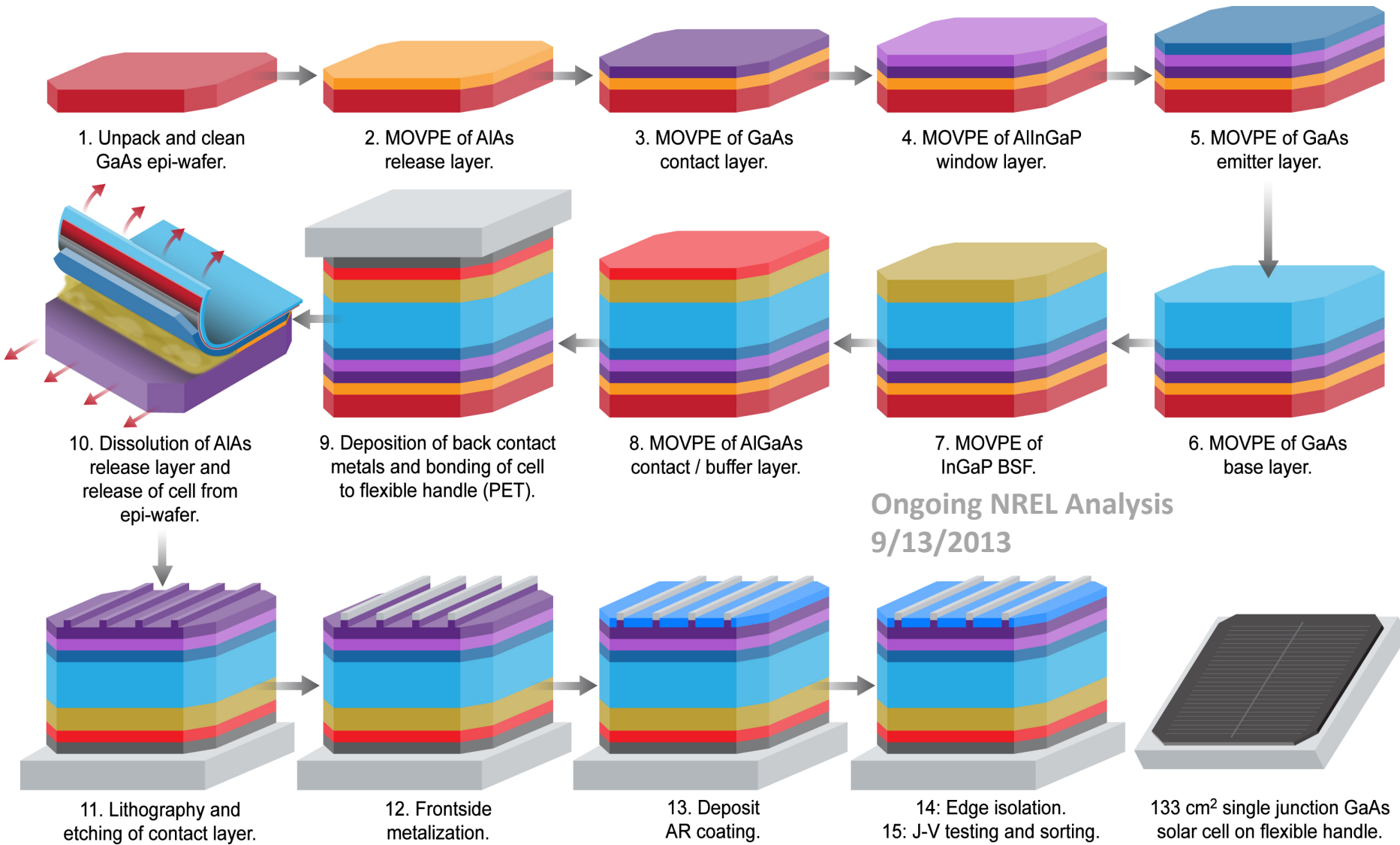
**Graphics and Communications**

**Alfred Hicks**  
**Kendra Palmer**  
**Nicole Harrison**

**September 30, 2013**

**Publication Number:**  
**NREL/PR-6A20-60126**  
**Contract Number:**  
**NREL: DE-AC36-08GO28308**

# An Example Process Flow for Making Single-Junction III-V Devices by ELO



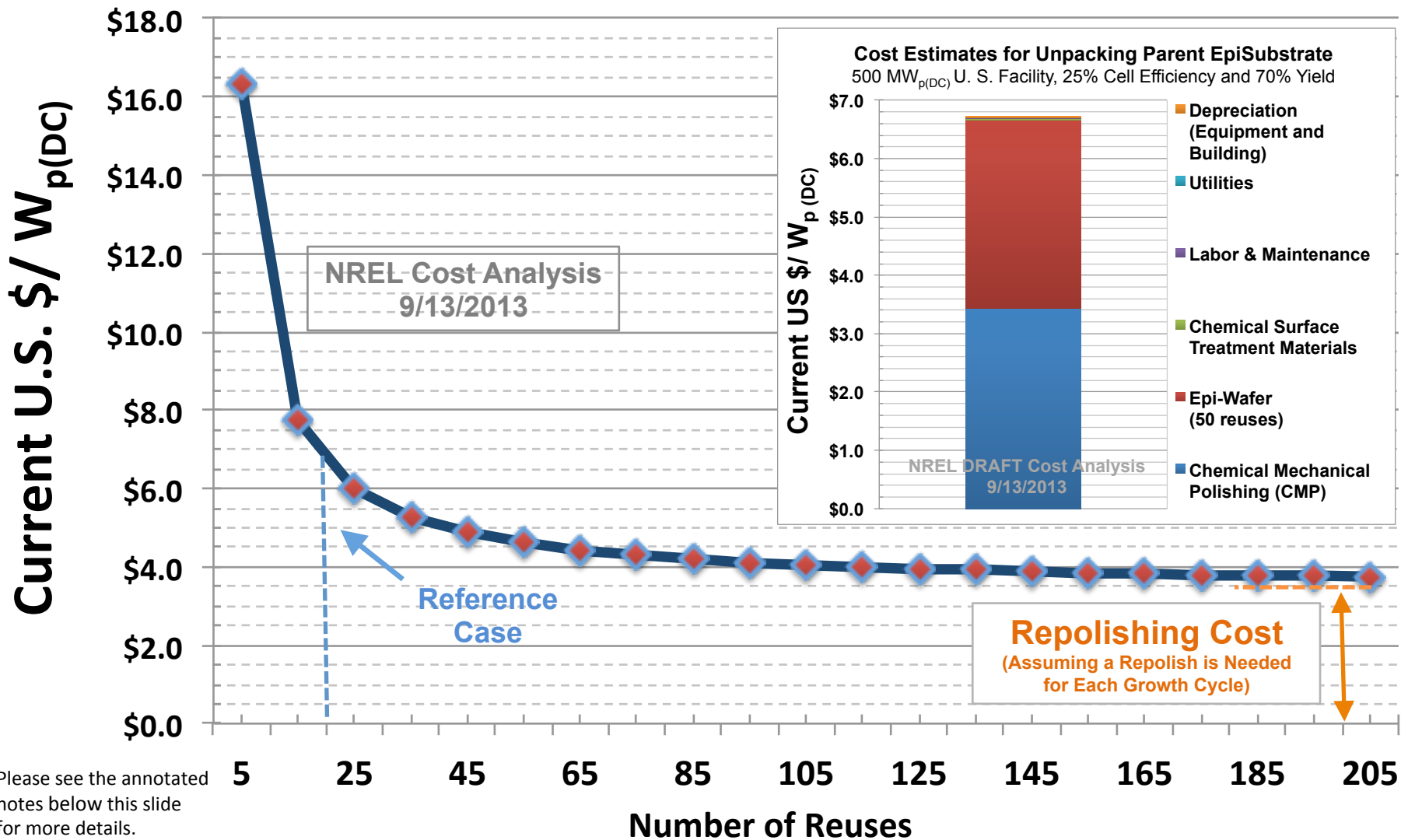
Ongoing NREL Analysis  
9/13/2013

# Step 1: Unpack and Clean GaAs Parent Epi-Substrate—3

(The Reference Case Scenario in the Bar Chart Assumes 50 Reuses and 70% Yields)

## The Costs for the Epi Substrate as a Function of Reuse Number

Reference Case Repolishing Cost (\$8 per repolish per 133 cm<sup>2</sup> wafer) and Cell Efficiency (25%)



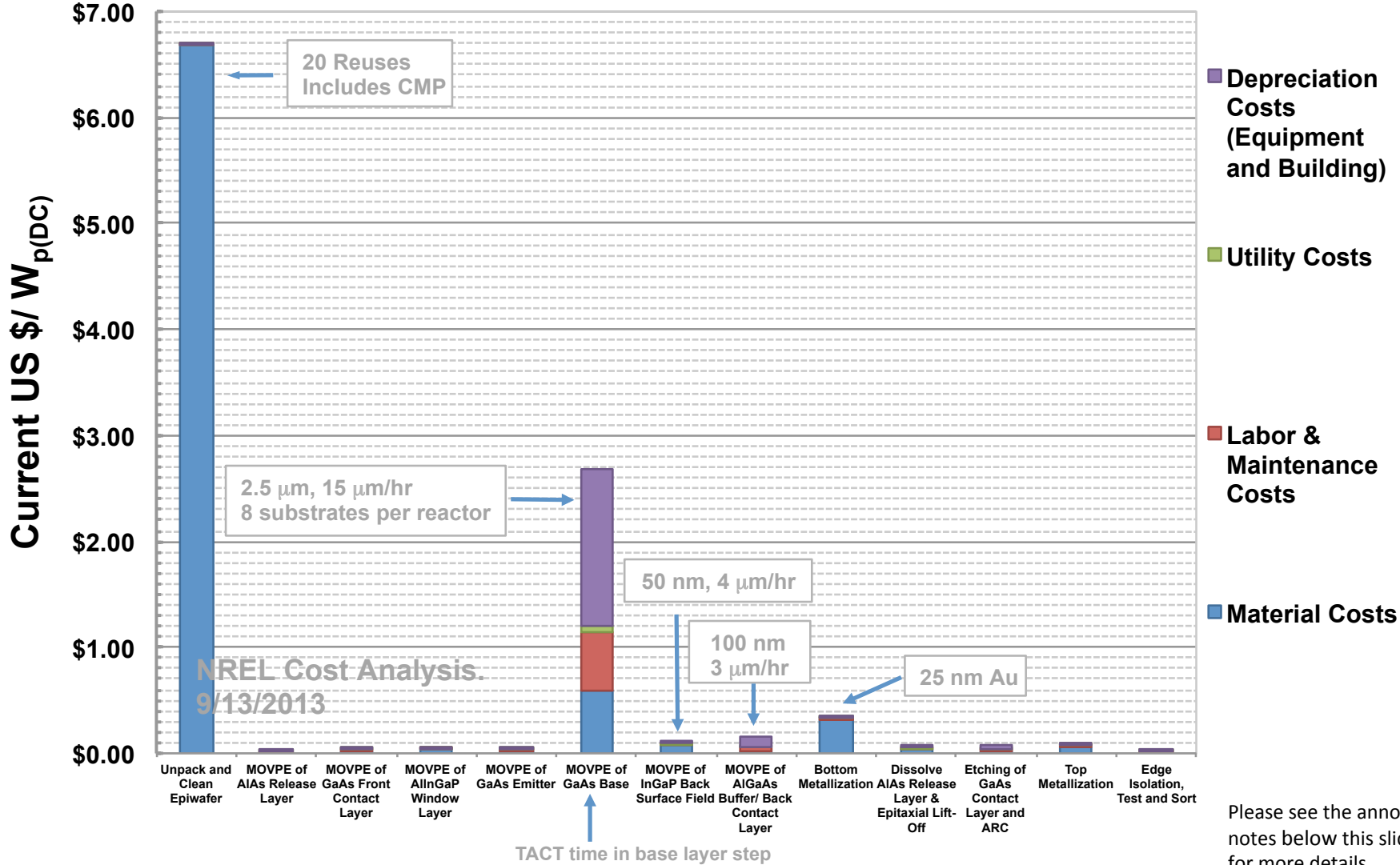
Please see the annotated notes below this slide for more details.

# Cost Summary, by Step, for the Reference Case.

(20 substrate reuses, precursor utilizations of 30% for the III- source and 20% for the V- source, 15  $\mu\text{m/hr}$  GaAs, 70% effective cell yield )

## Calculated Device Processing Costs for Single-Junction III-V's

500 MW<sub>P(DC)</sub> U.S. Facility, 25% Cell Efficiency, 70% Yield, 5 yrs Equipment Depreciation

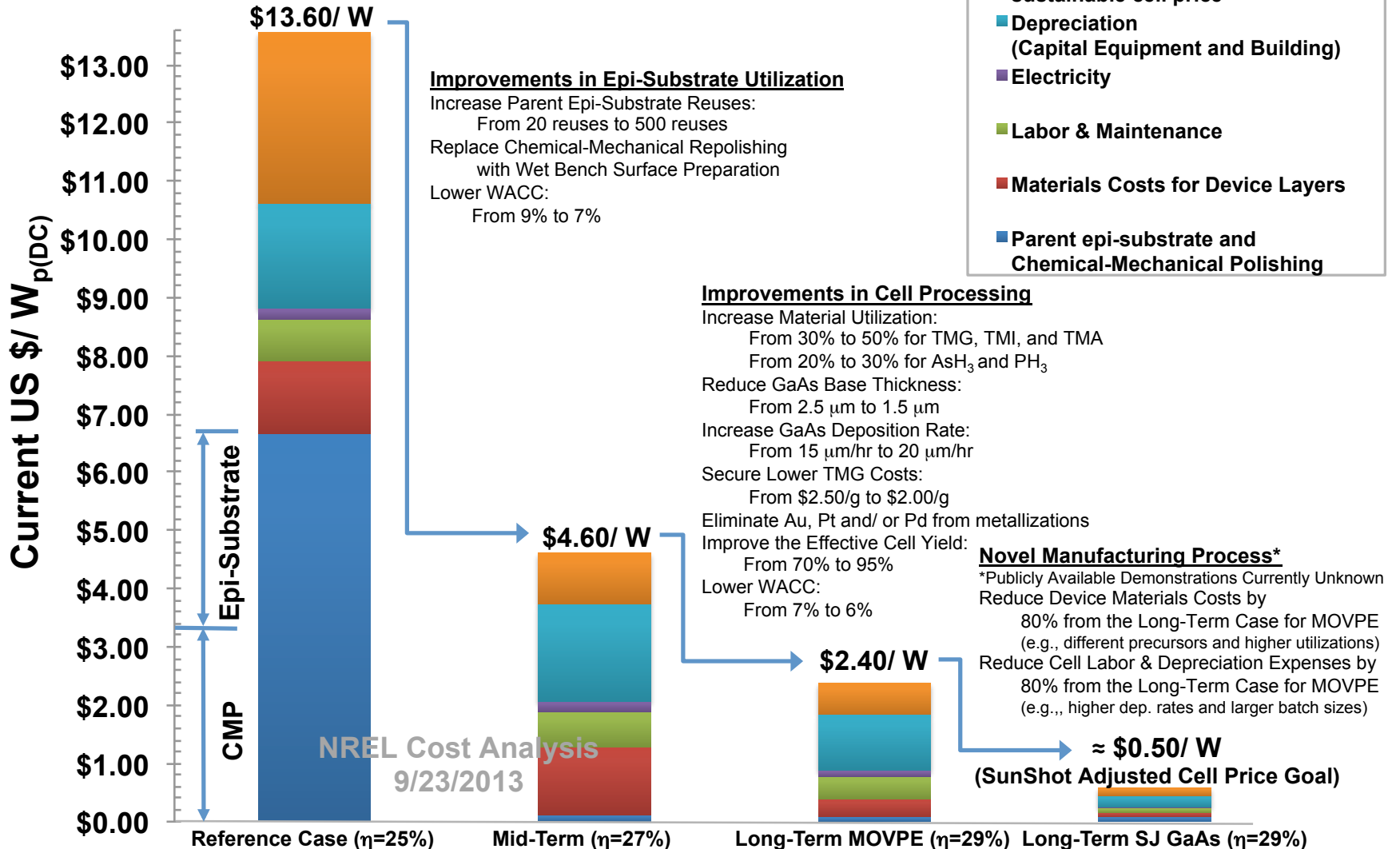


Please see the annotated notes below this slide for more details.

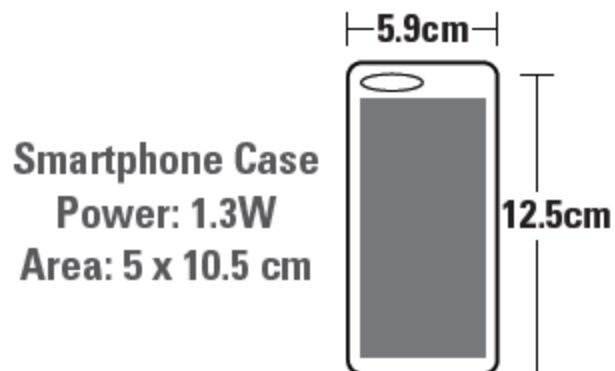
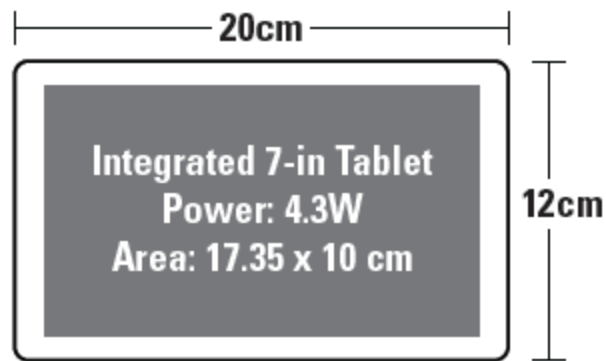
# Technology Roadmap Simulations for Single-Junction III-V's (GaAs Base)

## Cost Model Results for Single-Junction (SJ) GaAs Solar Cells by ELO

\$150 for 133 cm<sup>2</sup> Substrates, 0.25 Laborers per Reactor, U.S. Manufacturing  
 All stated efficiencies are AM 1.5G and 1000 W/m<sup>2</sup>



### Sample Products



Tablet and Smartphone Cases

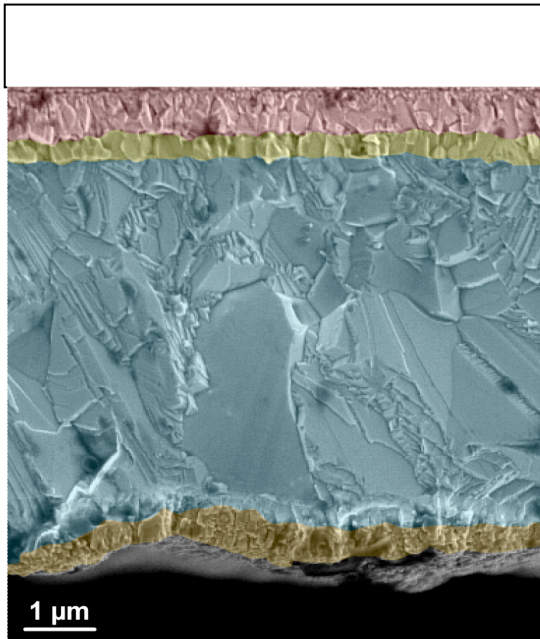




# What can be done to bring the costs down?

- Huge breakthrough in reducing materials deposition cost.
- Light trapping to reduce film thickness. See Jim Harris's 2013 GCEP talk at <http://gcep.stanford.edu/symposium>.
- Use concentrators. With epitaxial liftoff, 500 X concentrators might not be necessary. Trackers for 10 X concentrators are relatively cheap.

# Cadmium Telluride Solar Cells



CdS/CdTe

glass

SnO<sub>2</sub>

CdS

CdTe

ZnTe:Cu

Ti

- Direct bandgap,  $E_g = 1.45\text{eV}$
- High module production speed
- Very inexpensive
- 20.4 % efficiency

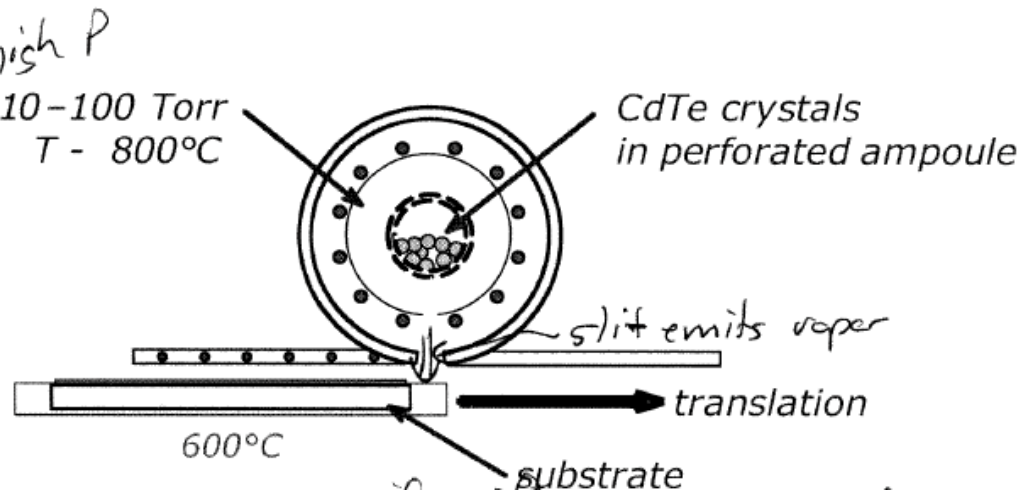
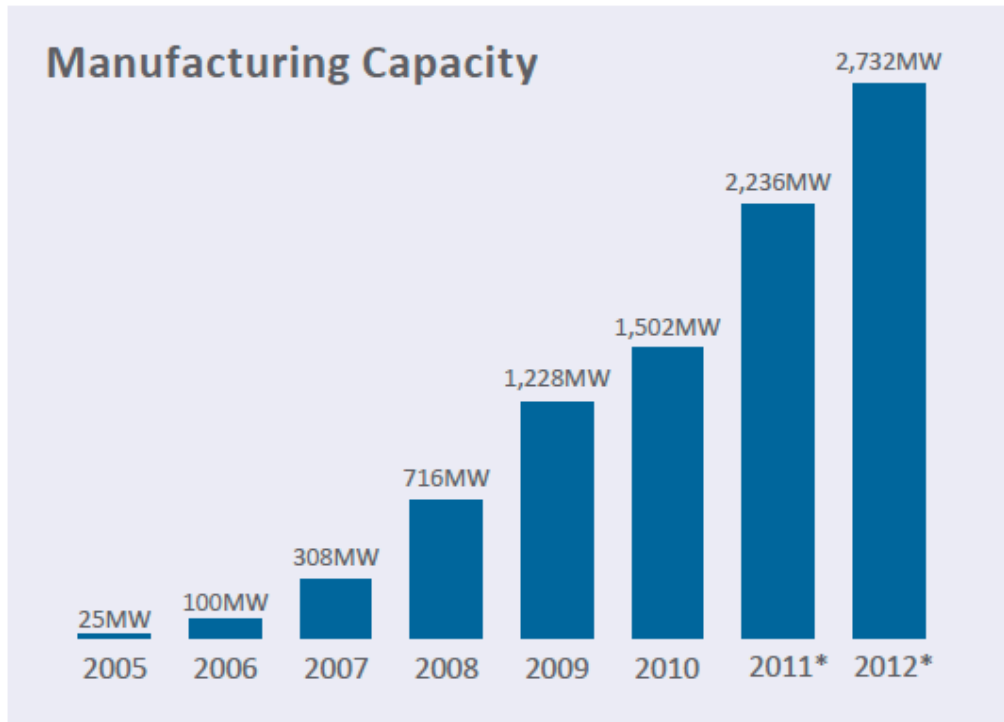


Image from Rommel Noufi  
 Schematic from Bulent Basol

# CdTe: Industrial Status

First Solar is the leader. It takes them 2.5 hours to make a 13.4 % module.

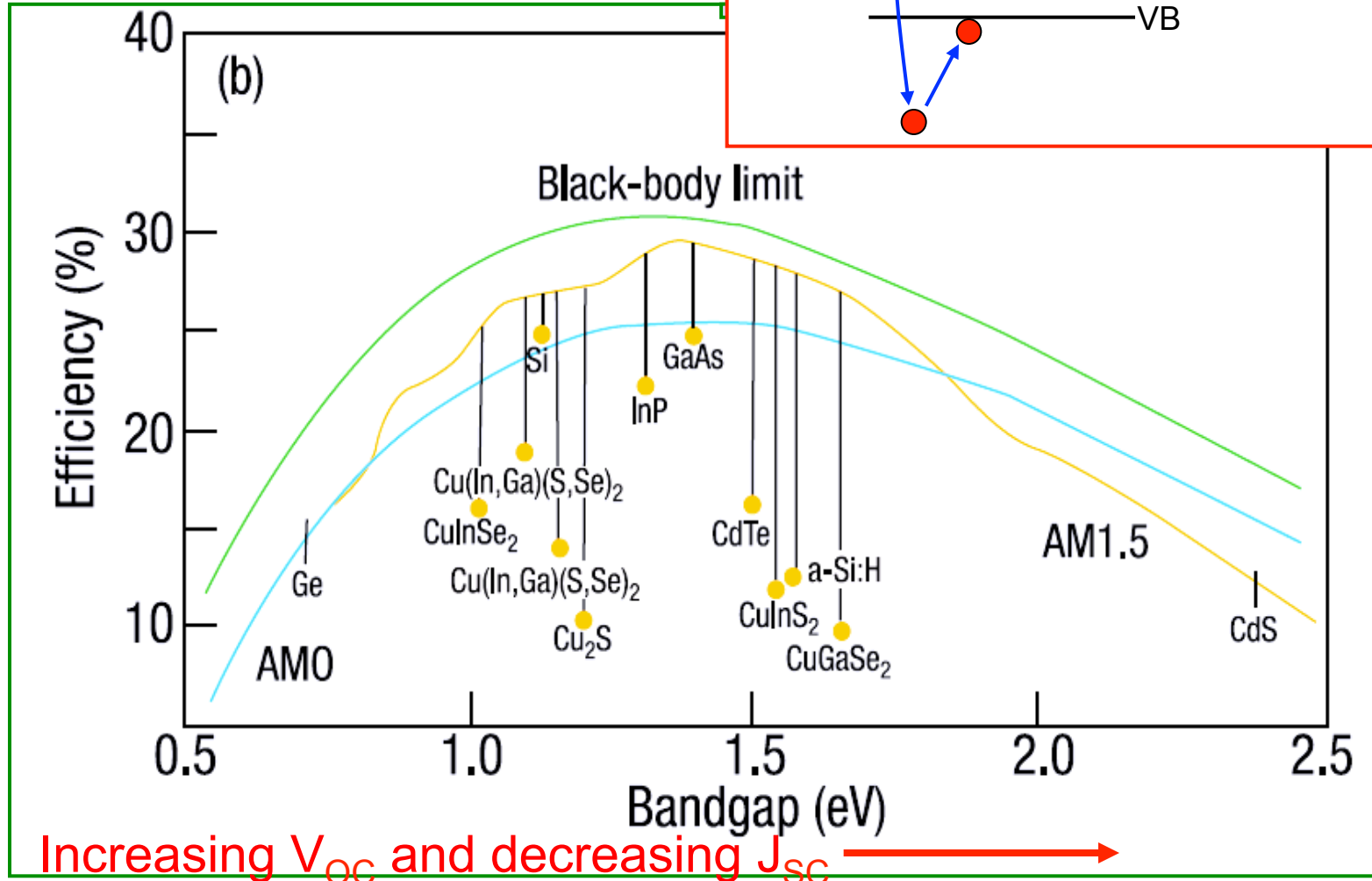
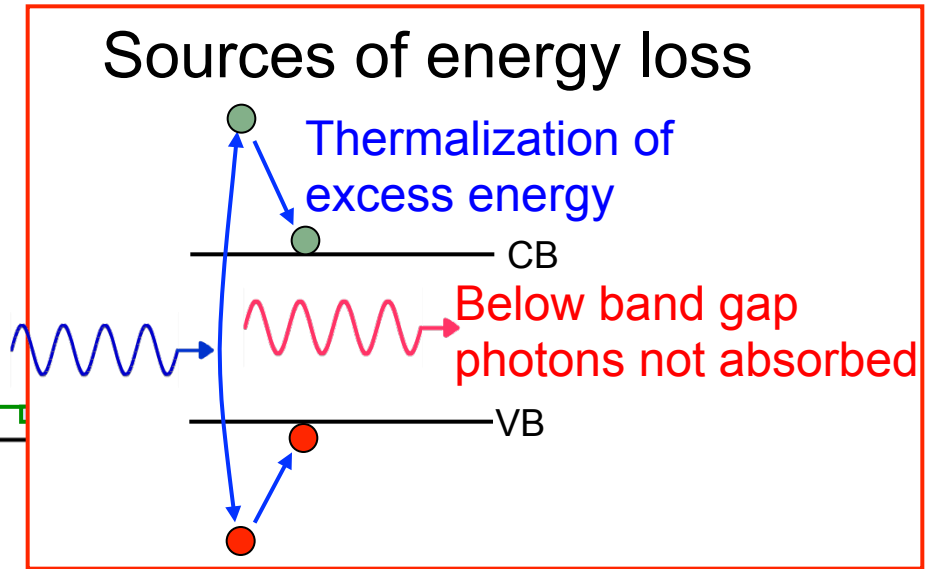


## Average Manufacturing Cost

2006: \$1.40/watt  
2007: \$1.23/watt  
2008: \$1.08/watt  
2009: \$0.87/watt  
2010: \$0.77/watt  
2011: \$0.74/watt  
2012: \$0.64/watt  
2013: \$0.53/watt

The energy payback time is 0.8 years.

# Efficiency limits



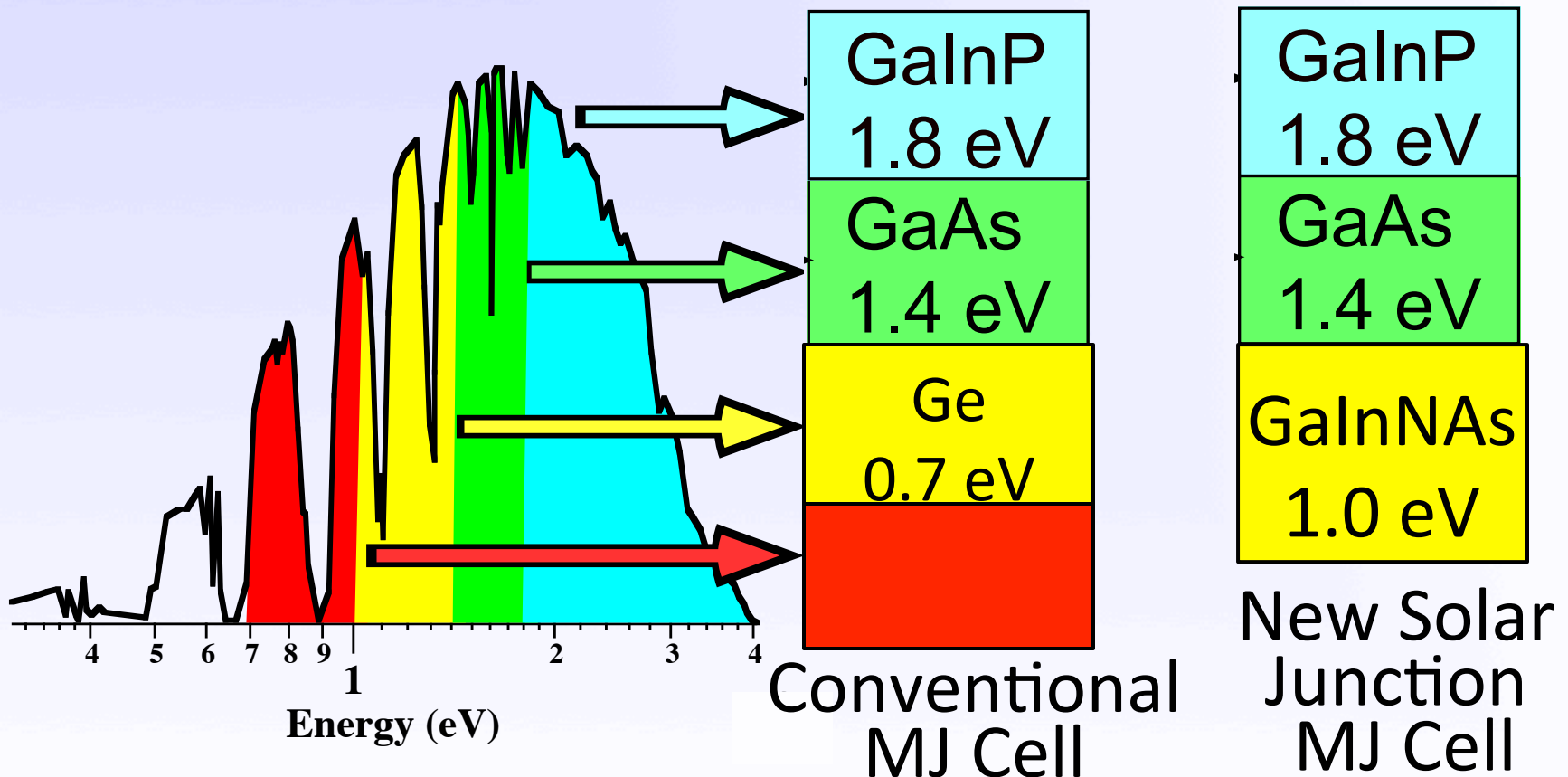
There are lots of 3<sup>rd</sup> Generation ideas to beat the Shockley-Quiesser limit, but only one that works.

# Multijunctions: The Road to Higher Efficiencies

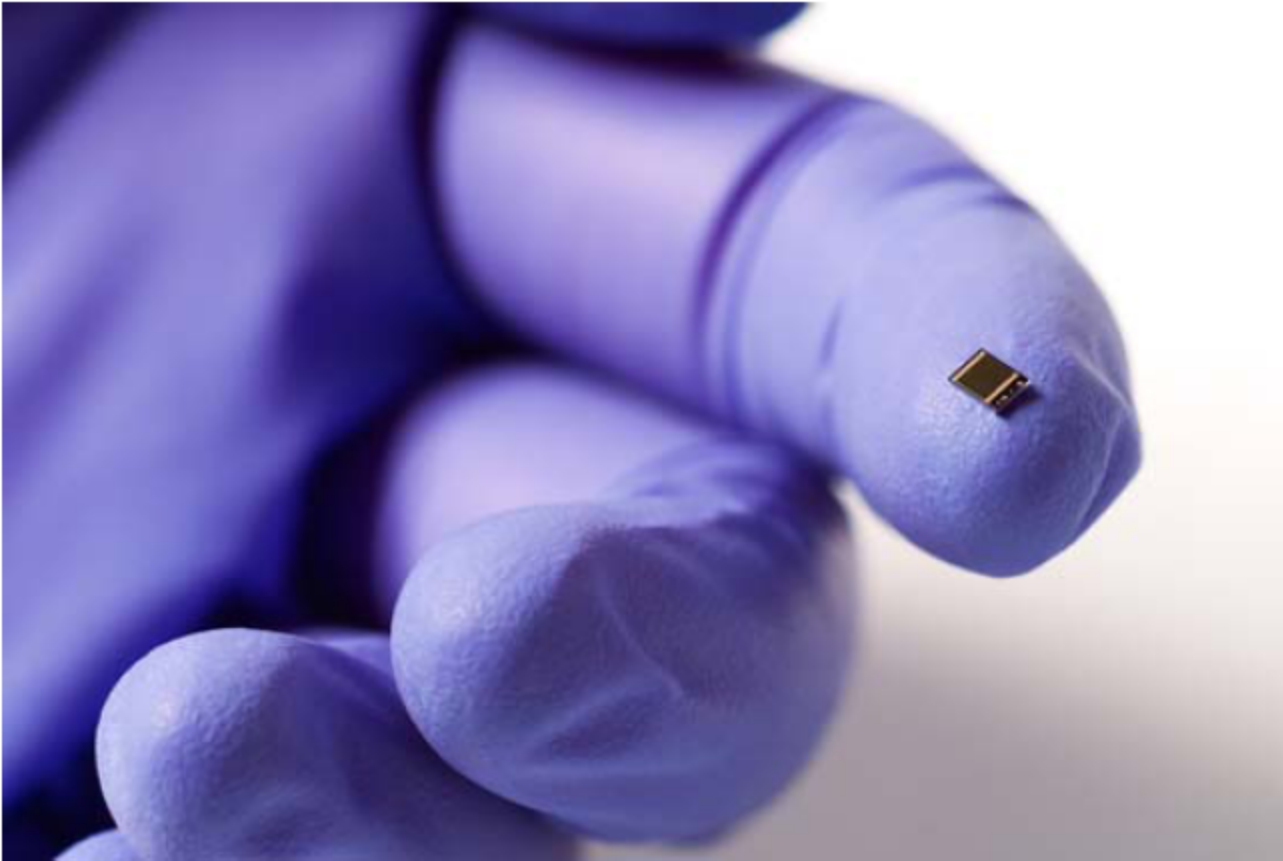


Higher-efficiency MJ cells require new materials that divide the solar spectrum equally to provide current match

Ge provides lattice match but the bandgap is too small



## 4-junction cell with 44.7 % efficiency at 297 suns

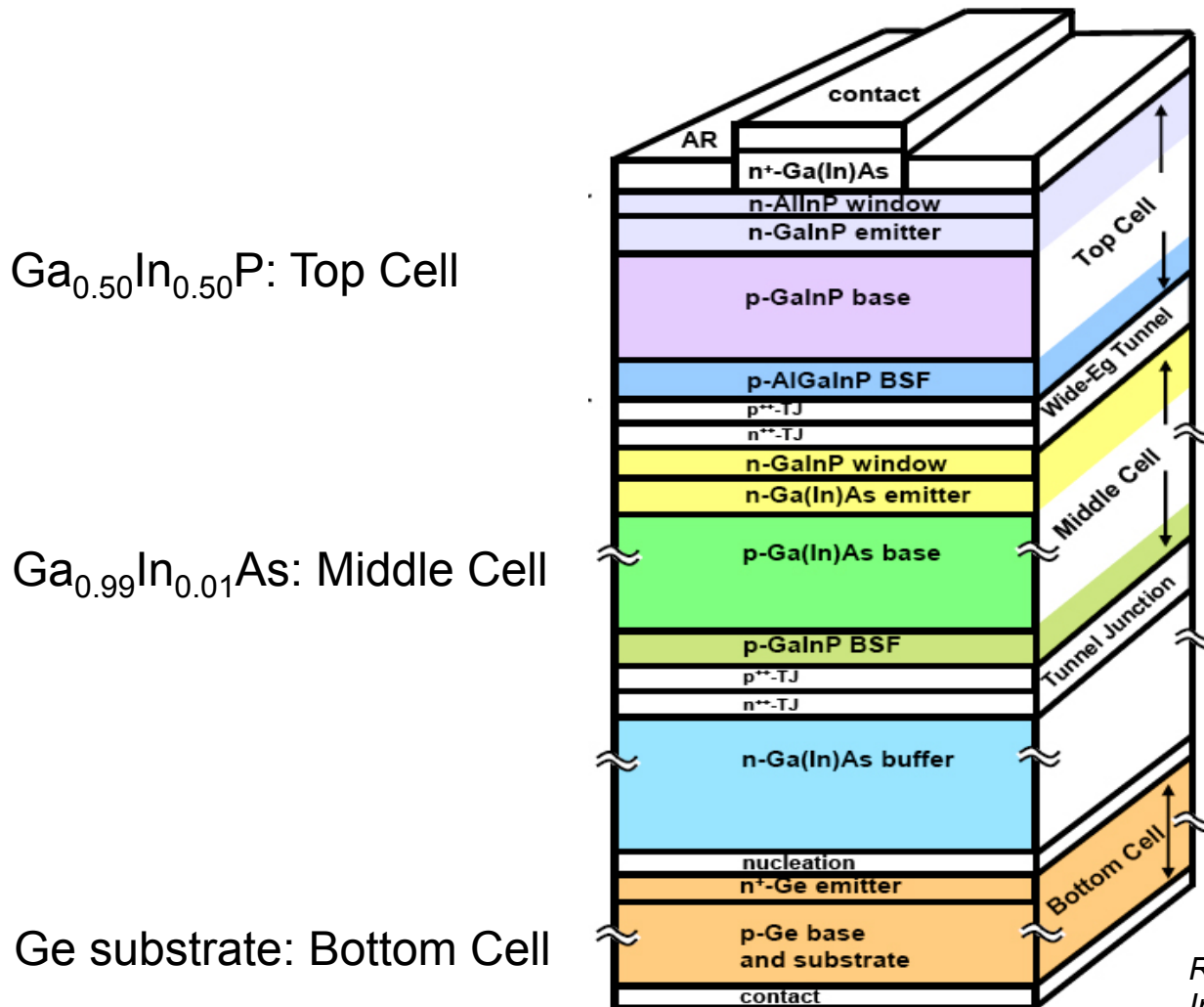


World record solar cell with 44.7% efficiency, made up of four solar subcells based on III-V compound semiconductors for use in concentrator photovoltaics.

©Fraunhofer ISE



# Multijunction Cells are Very Expensive



- These complex structures are grown very slowly under high vacuum.

- 37 % cells can be purchased for \$50,000/m<sup>2</sup>

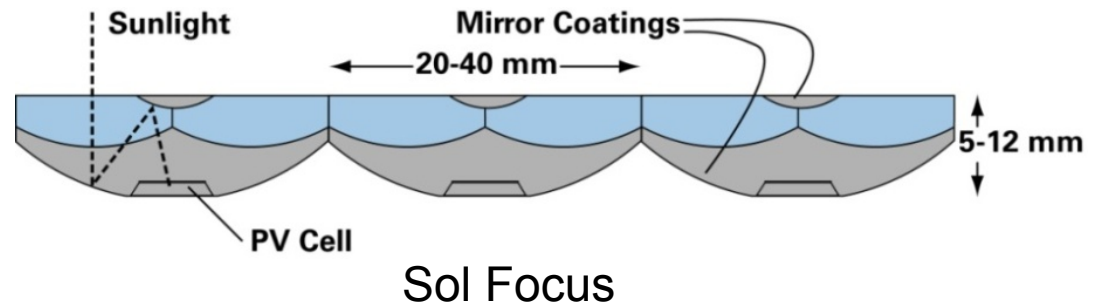
- Concentrating the light is essential.

# Concentrating Light

It is possible to track the sun and concentrate the light by 500X



Dish Shape



# Hybrid Tandems Are Intended to be a High-Performance Low-Cost Option

Efficiency

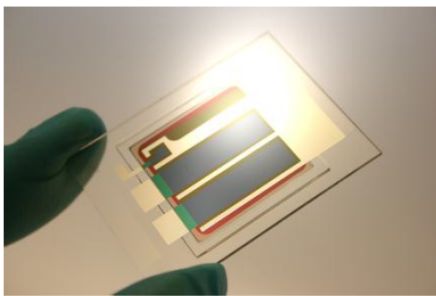
Cost



**Organic**

12% efficient

\$30/m<sup>2</sup>



**Hybrid**

30% efficient

\$100/m<sup>2</sup>

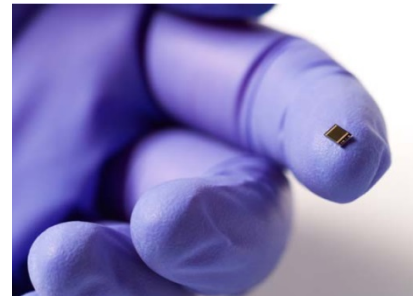


|   |
|---|
| <p><b>Low Cost Defect-Tolerant Technology:</b><br/>Perovskite, Organic,<br/>Nanowires or II-VI<br/><math>E_g \sim 1.9 \text{ eV}</math></p> |
| <p><b>Established Technology:</b><br/>Silicon or CIGS<br/><math>E_g \sim 1.1 \text{ eV}</math></p>  |

**Epitaxial Crystalline**

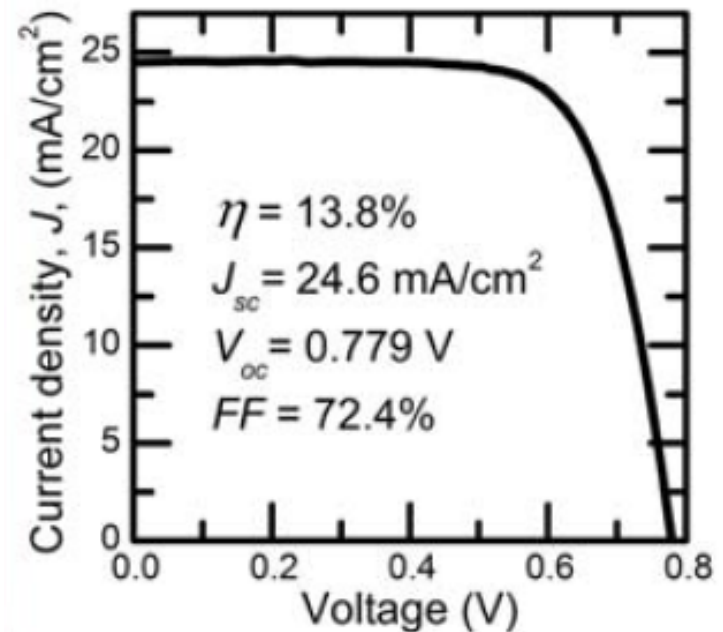
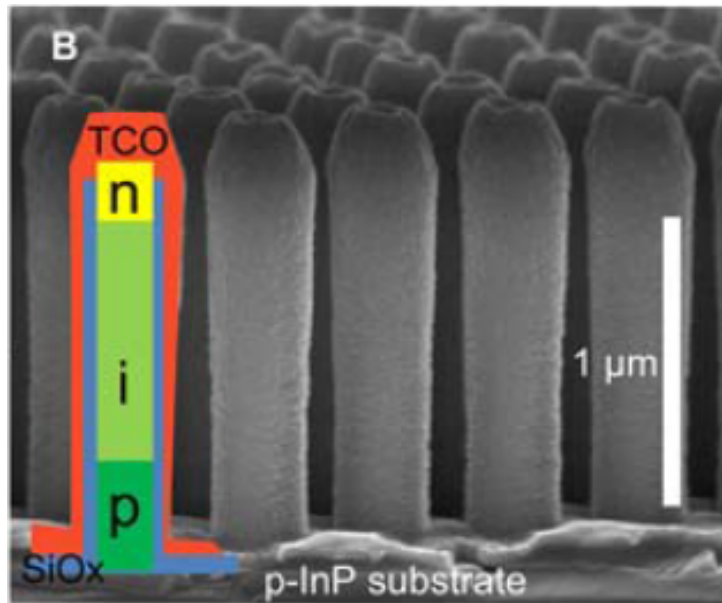
45 % efficient

\$40,000/m<sup>2</sup>



# InP Nanowire Array Solar Cells Achieving 13.8% Efficiency by Exceeding the Ray Optics Limit

Jesper Wallentin,<sup>1</sup> Nicklas Anttu,<sup>1</sup> Damir Asoli,<sup>2</sup> Maria Huffman,<sup>2</sup> Ingvar Åberg,<sup>2</sup> Martin H. Magnusson,<sup>2</sup> Gerald Siefert,<sup>3</sup> Peter Fuss-Kailuweit,<sup>3</sup> Frank Dimroth,<sup>3</sup> Bernd Witzigmann,<sup>4</sup> H. Q. Xu,<sup>1,5</sup> Lars Samuelson,<sup>1</sup> Knut Deppert,<sup>1</sup> Magnus T. Borgström<sup>1\*</sup>



Surface recombination velocity = 170 cm/s

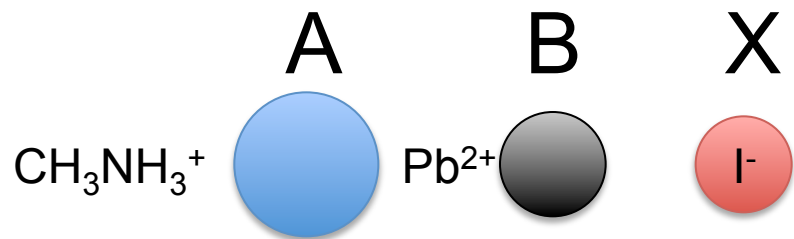
*Science* 339 (2013) p. 1057.

Stion, Khosla-Funded PV Startup, Hits 23.2%  
Efficiency With Tandem CIGS

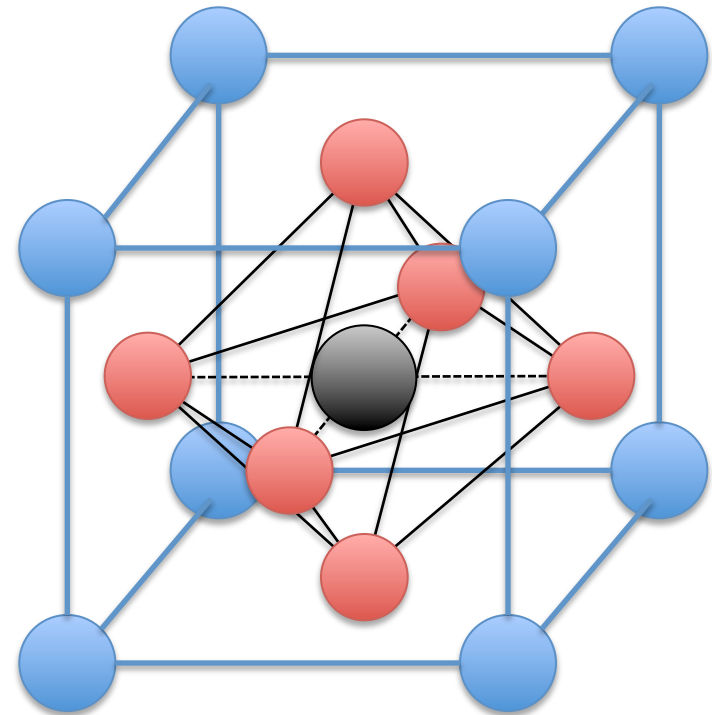
Greentech Media  
February 24, 2014

# 'Perovskite' Describes a Crystal Structure Class

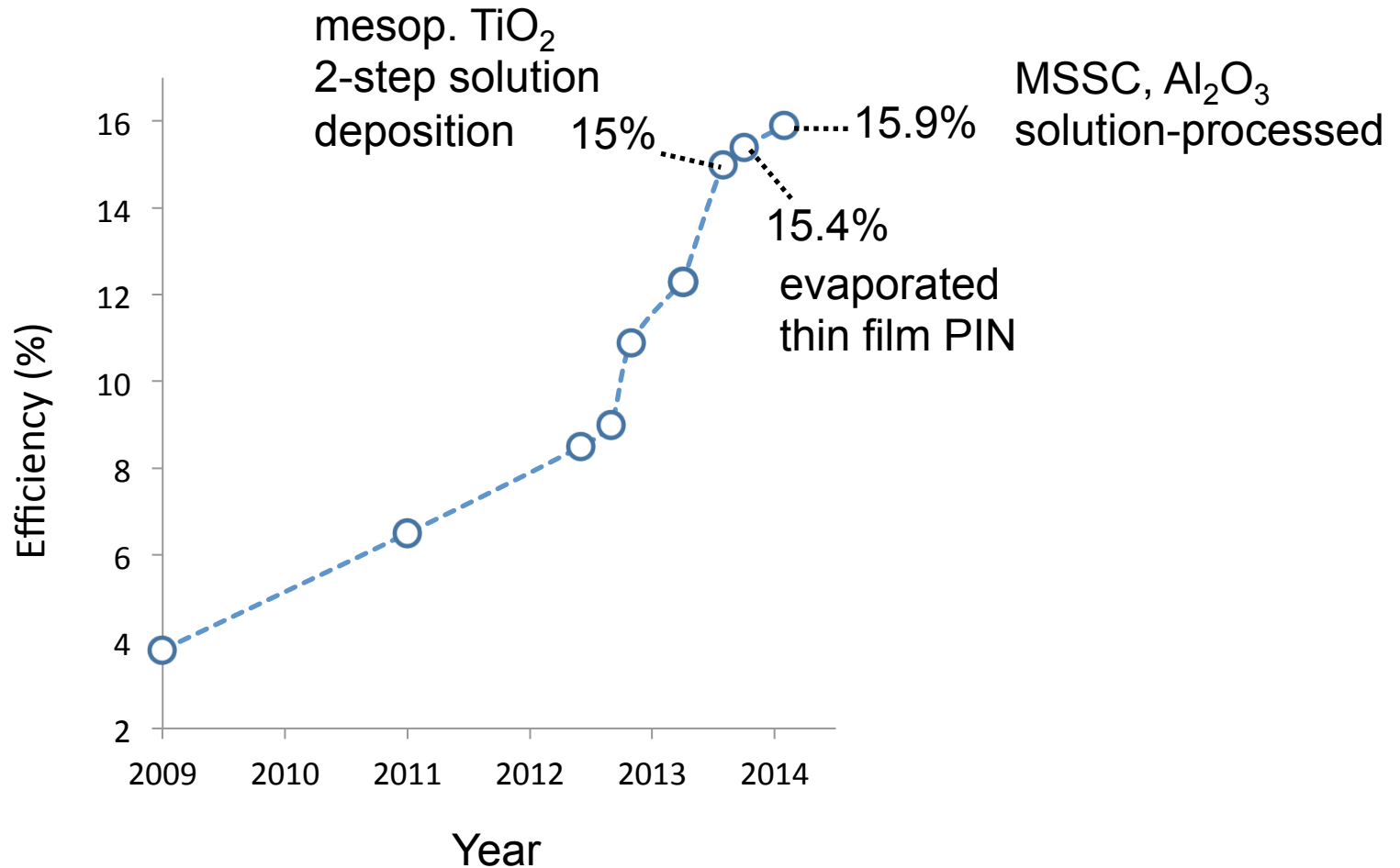
Generic formula:  $ABX_3$



*Methylammonium-lead-iodide*



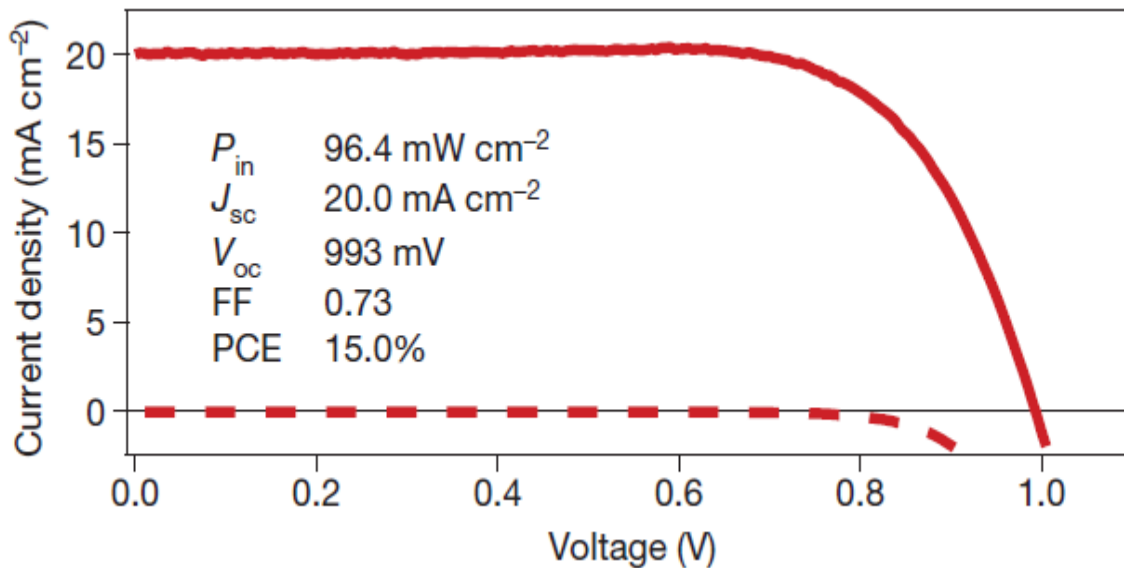
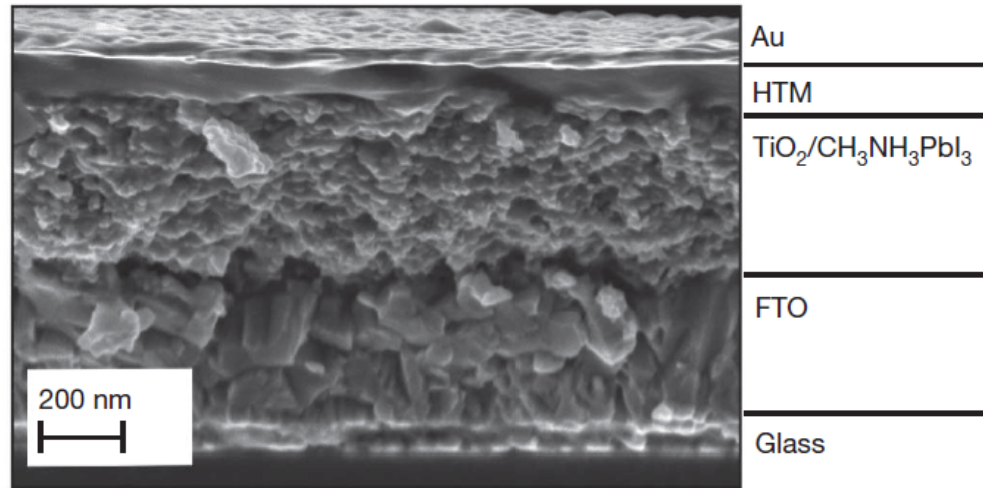
# Perovskite Solar Cells are Soaring



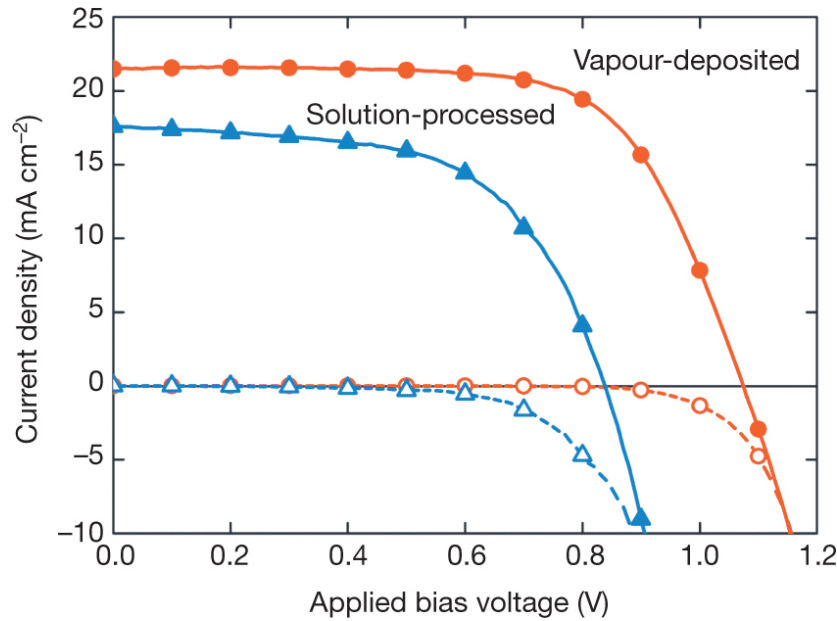
Snaith et al., *En. Env Sci.* Jan 2014  
Snaith et al., *Nature* Sep 2013  
Grätzel et al., *Nature* Jul 2013



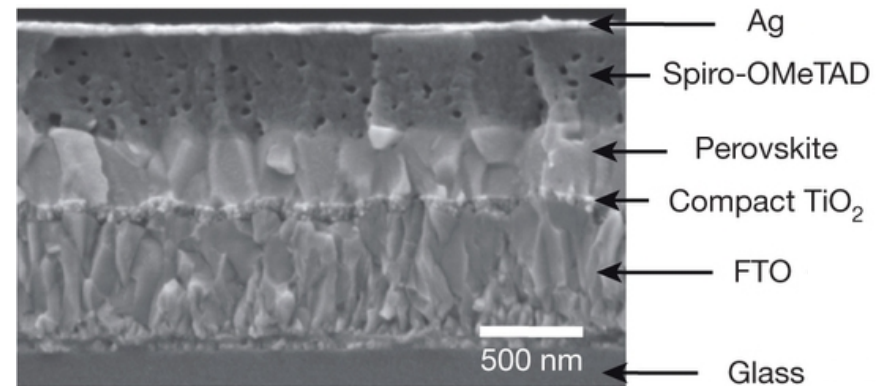
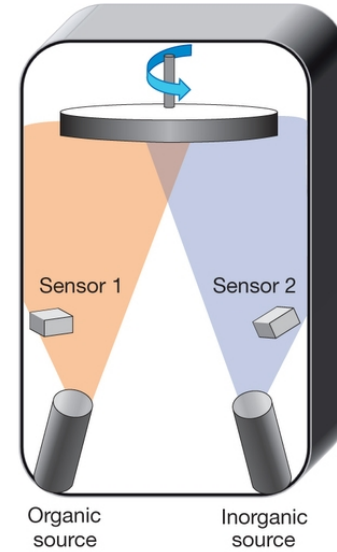
# Perovskite Solar Cells Evolved From the Dye-Sensitized Solar Cell



# Perovskites Are Compatible With A Planar P-I-N Architecture



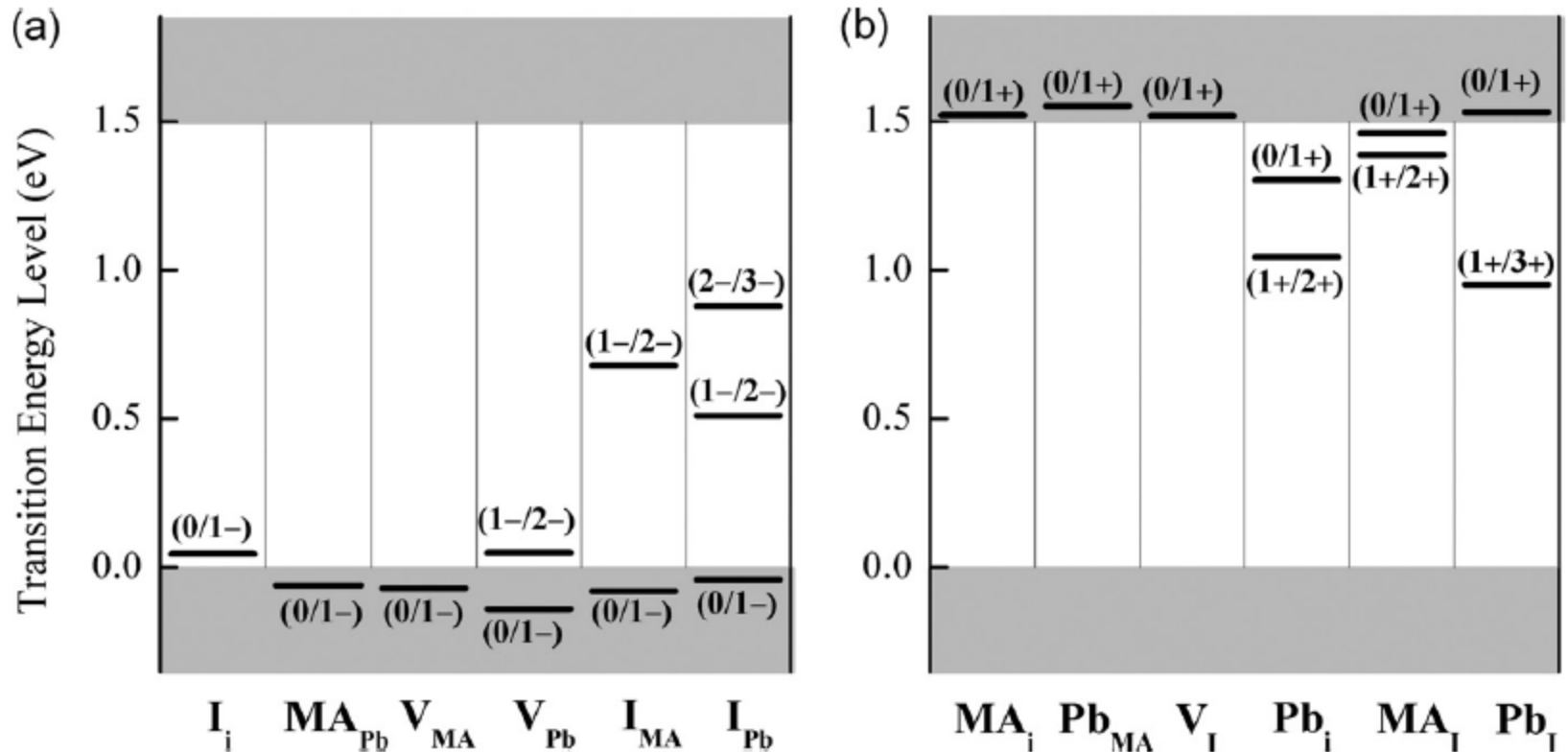
**$J_{sc} = 21.5 \text{ mA/cm}^2$**   
 **$V_{oc} = 1.07 \text{ V}$**   
 **$FF = 0.68$**   
 **$\eta = 15.4\%$**



# Low Bandgap – $q \cdot V_{oc}$ Loss in Perovskite Solar Cells

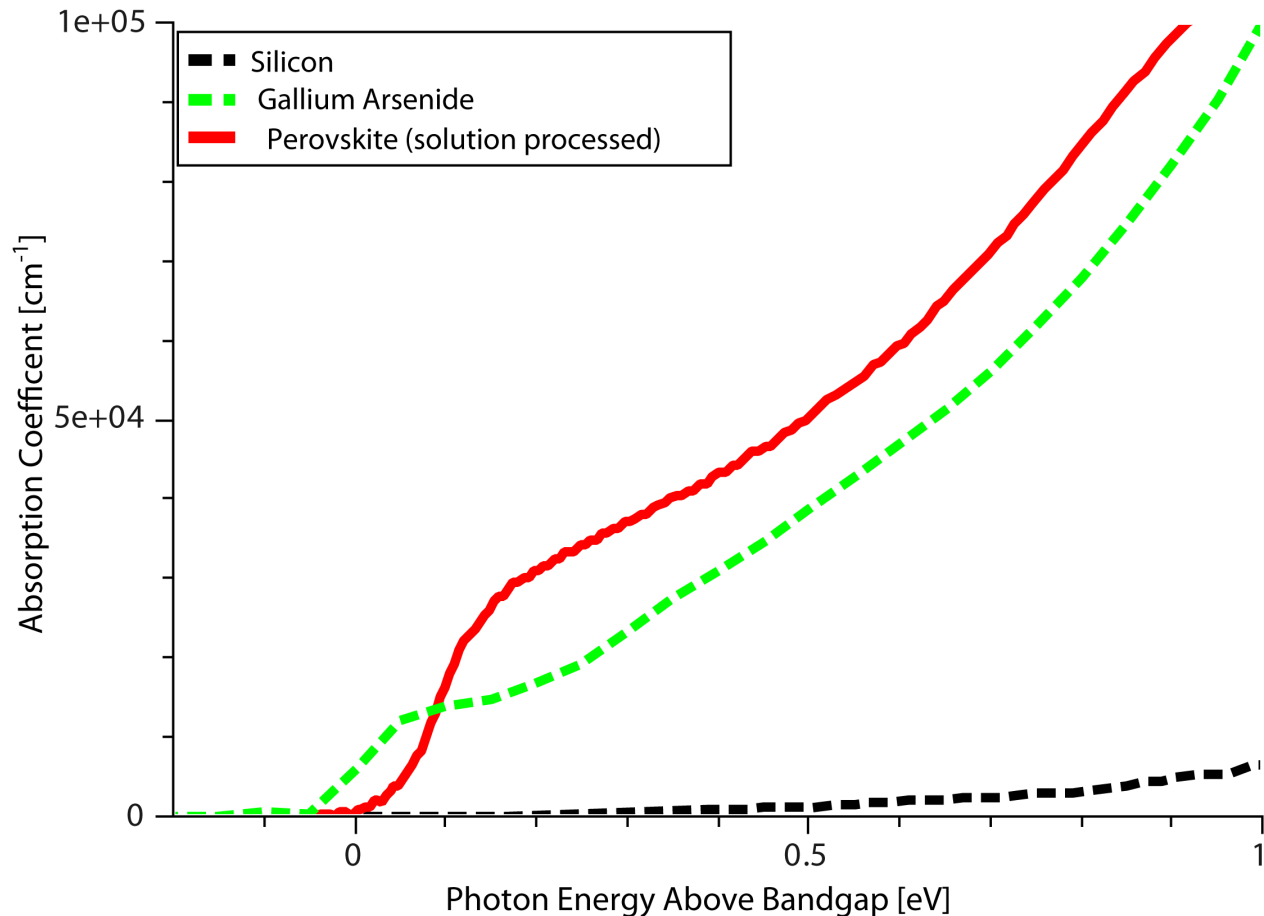
| Material  | Bandgap (eV) | $q \cdot V_{oc}$ (eV) | Energy loss (eV) |
|---|--------------|-----------------------|------------------|
| GaAs  | 1.43         | 1.12                  | 0.31             |
| Silicon   | 1.12         | 0.75                  | 0.37             |
| CIGS  | ~1.15        | 0.74                  | 0.41             |
| <b>Perovskite<br/>(CH<sub>3</sub>NH<sub>3</sub>PbI<sub>3</sub>)</b> | <b>1.55</b>  | <b>1.07</b>           | <b>0.48</b>      |
| CdTe  | 1.49         | 0.90                  | 0.59             |
| a-Silicon   | 1.55         | 0.89                  | 0.66             |

# The traps are shallow



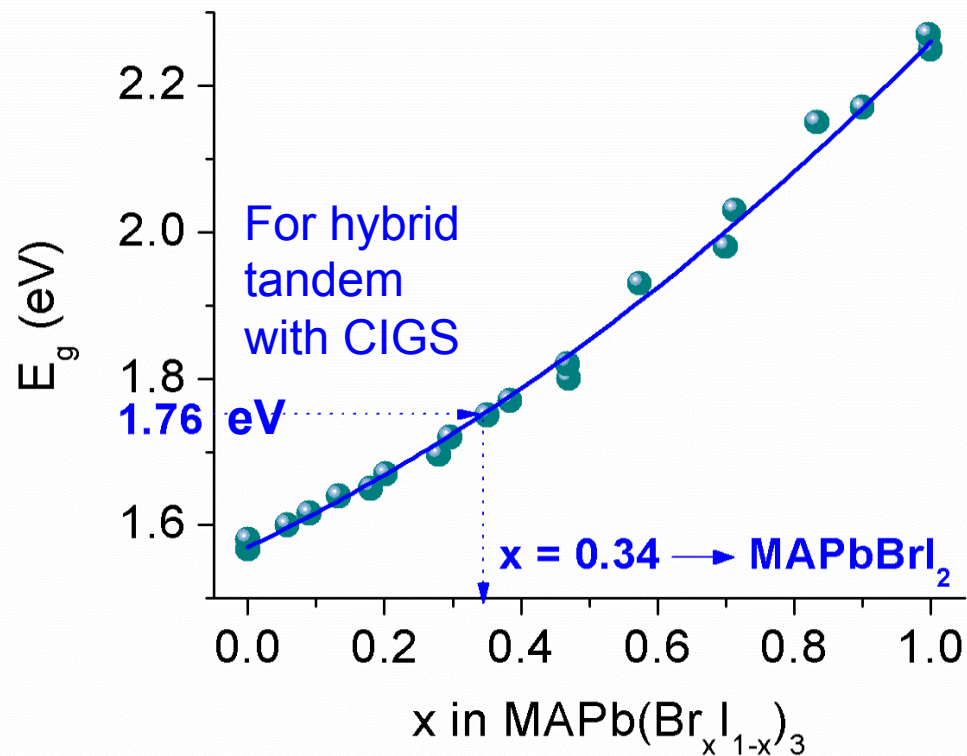
Yanfa Yan et al., *Appl. Phys. Lett.* 104 (2014) 63903.

# The Perovskite is a Strongly-Absorbing Direct Band Gap Semiconductor

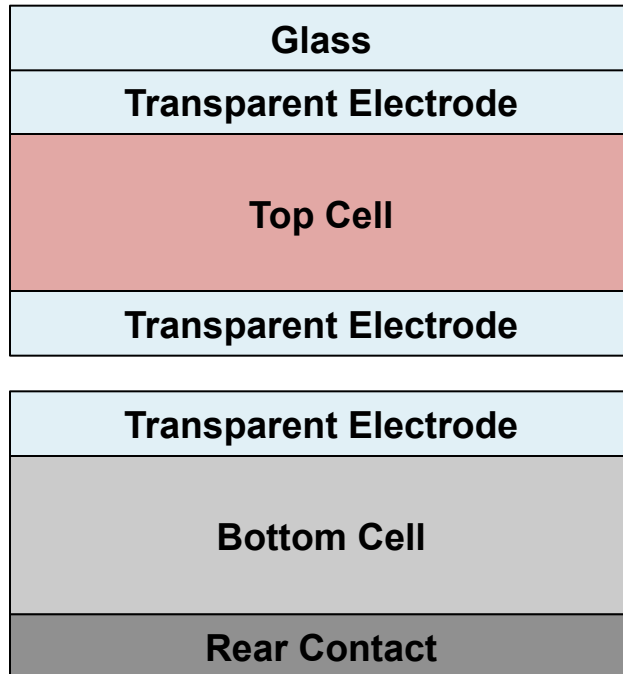
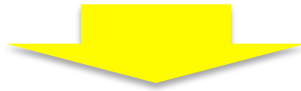


# The Perovskite Bandgap can be tuned by Chemical Substitution

The band gap can be tuned from 1.57 eV to 2.23 eV by substituting bromine for iodine in  $\text{CH}_3\text{NH}_3\text{Pb}(\text{Br}_x\text{I}_{1-x})_3$

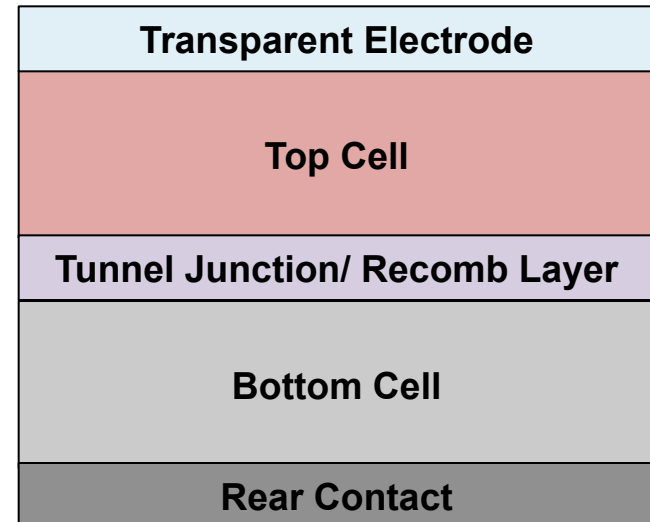
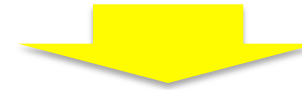


# Hybrid Tandem Architectures



## 4 Terminal

- Easier prototyping
- No current matching required
- No tunnel junction or recombination layer required

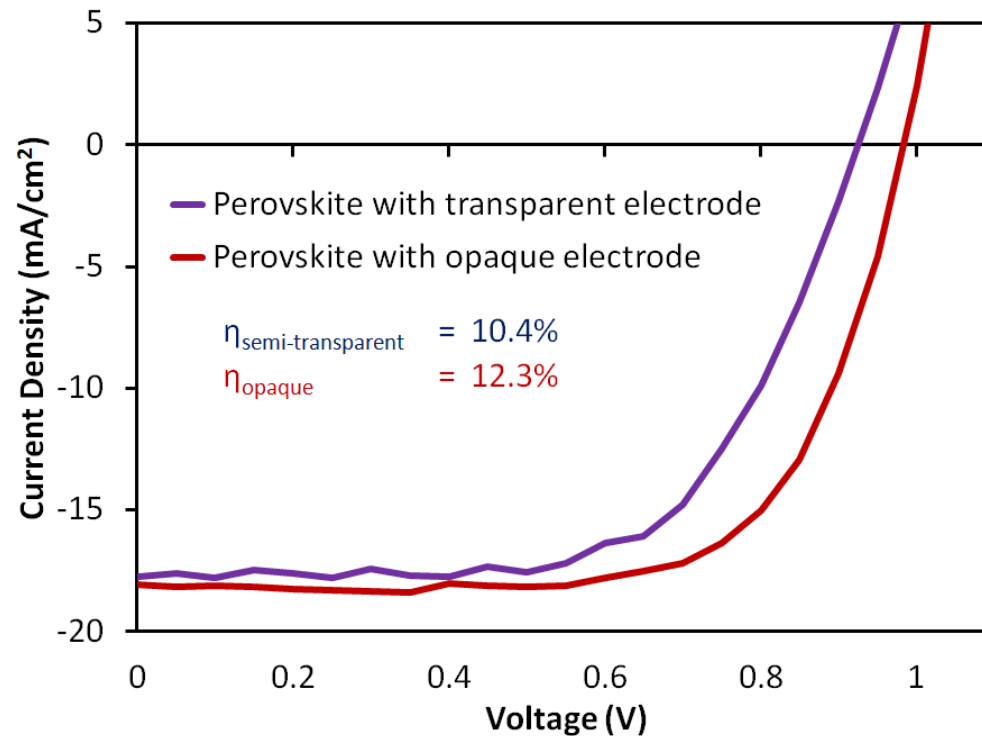


## 2 Terminal

- Fewer layers that parasitically absorb
- Module fabrication easier



# Our Semitransparent Perovskite Cells



Colin Bailie, Grey Christoforo

# Preliminary Cost Estimates

|            | Today' s Silicon     | Silicon-Perovskite   |
|------------|----------------------|----------------------|
| Efficiency | 19.4 %               | 25 %                 |
| Cost/Area  | \$153/m <sup>2</sup> | \$167/m <sup>2</sup> |
| Cost/Watt  | \$0.79/W             | \$0.67/W             |

Expected improvements in silicon technology will take the cost below \$0.5/W!

# Conclusions

- Conventional silicon leads the solar cell race, but will not take us where we need to go.
- Several technologies could take over in the next 10 years.
- We are still discovering new materials with substantially better properties.
- I think multijunction solar cells will be thin, light, cheap and  $> 30\%$  efficient.

# Final thoughts

- We have to solve the energy problem.
- Any technology that has good potential to cut carbon emissions by  $> 10\%$  needs to be explored aggressively.
- Researchers should not be deterred by the struggles some companies are having.
- Someone needs to invest in scaling up promising solar cell technologies.